

The ATLAS Pixel Detector: Overview & Commissioning

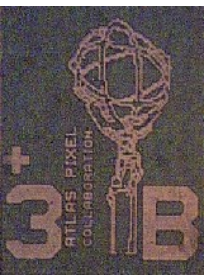
Andreas Korn

Lawrence Berkeley National Lab

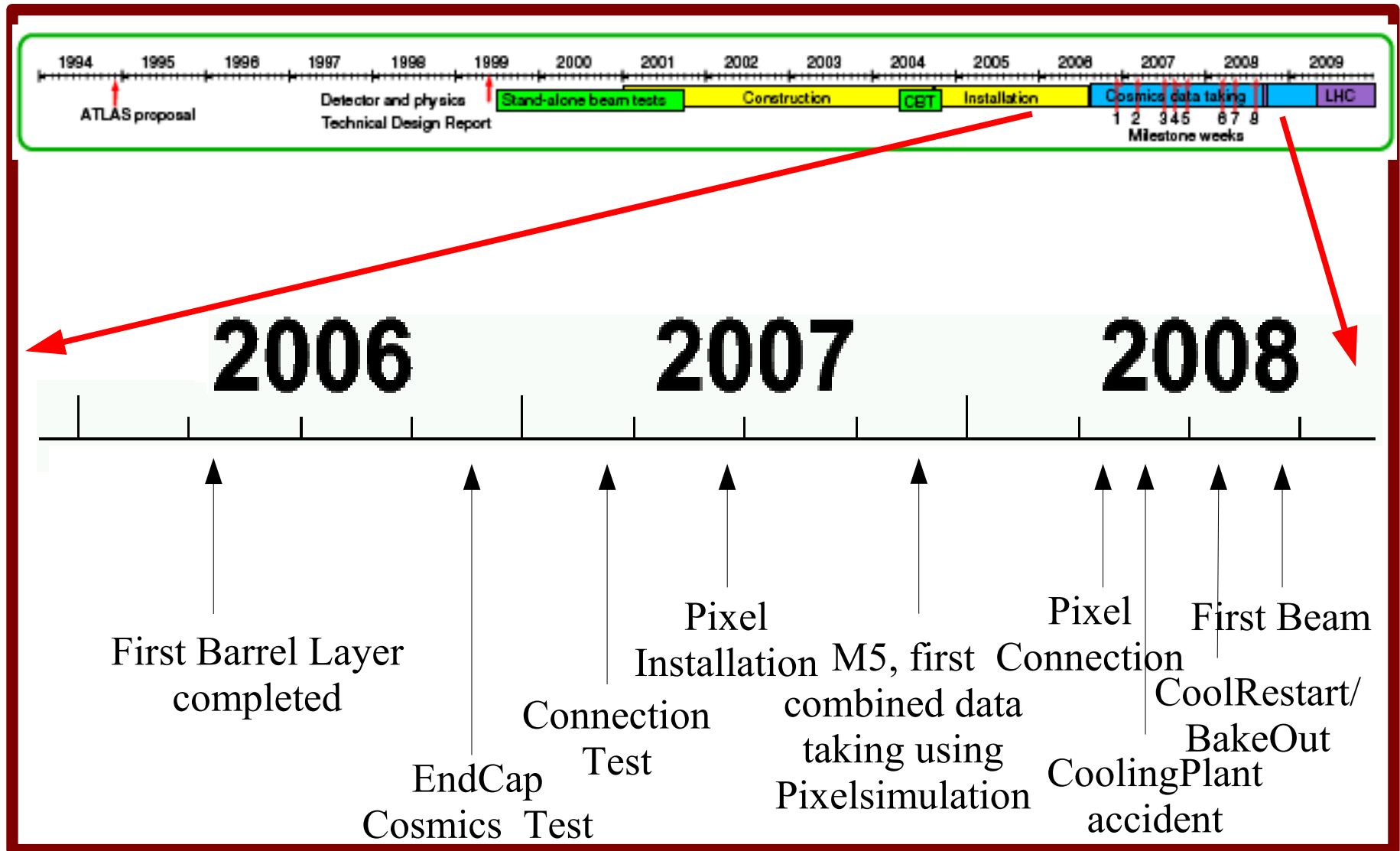
AKorn@lbl.gov

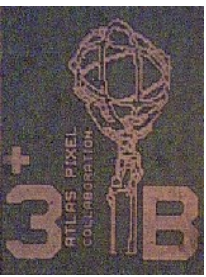
on behalf of the
ATLAS Pixel Collaboration



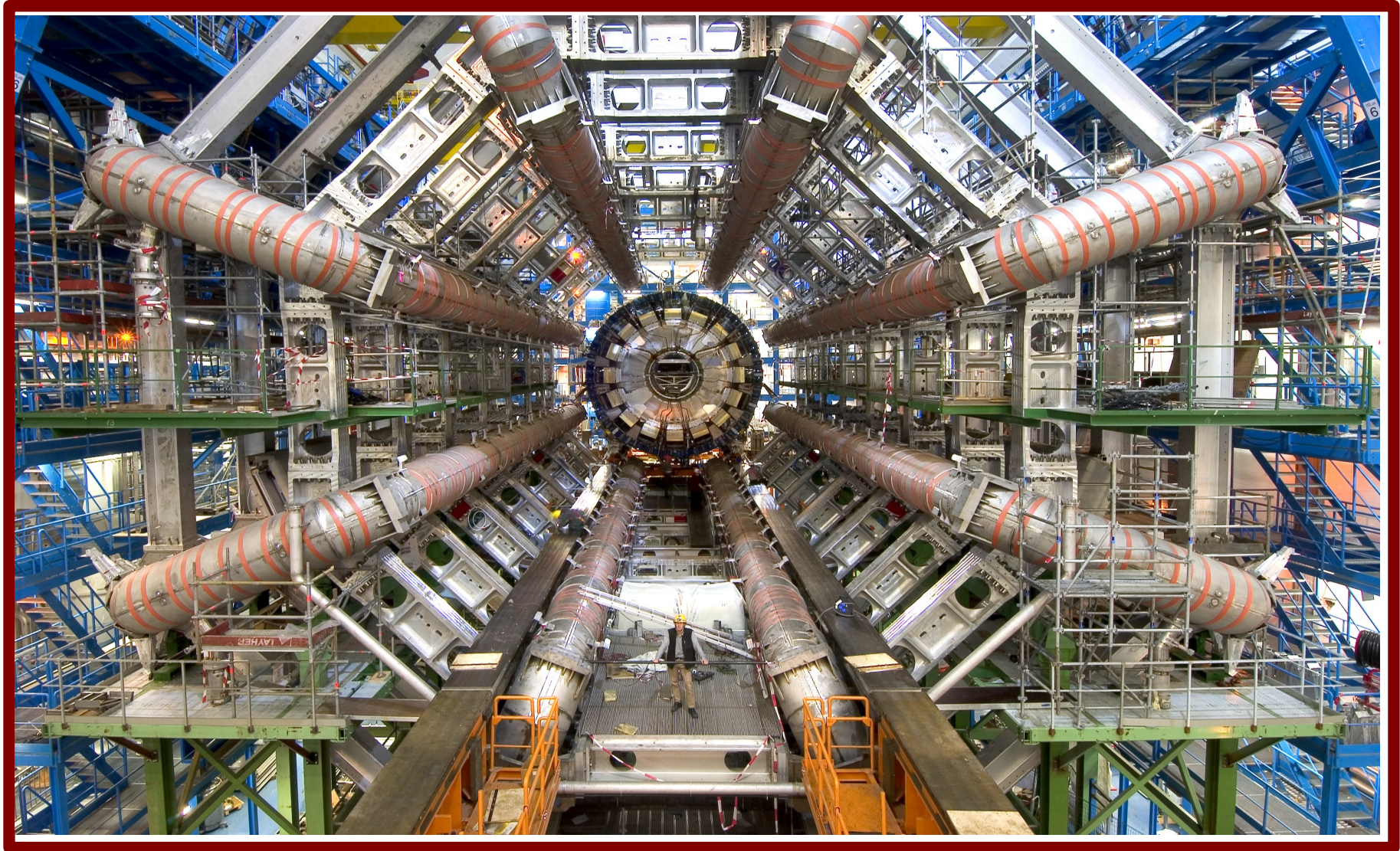


ATLAS ID timeline Overview





ATLAS Overview

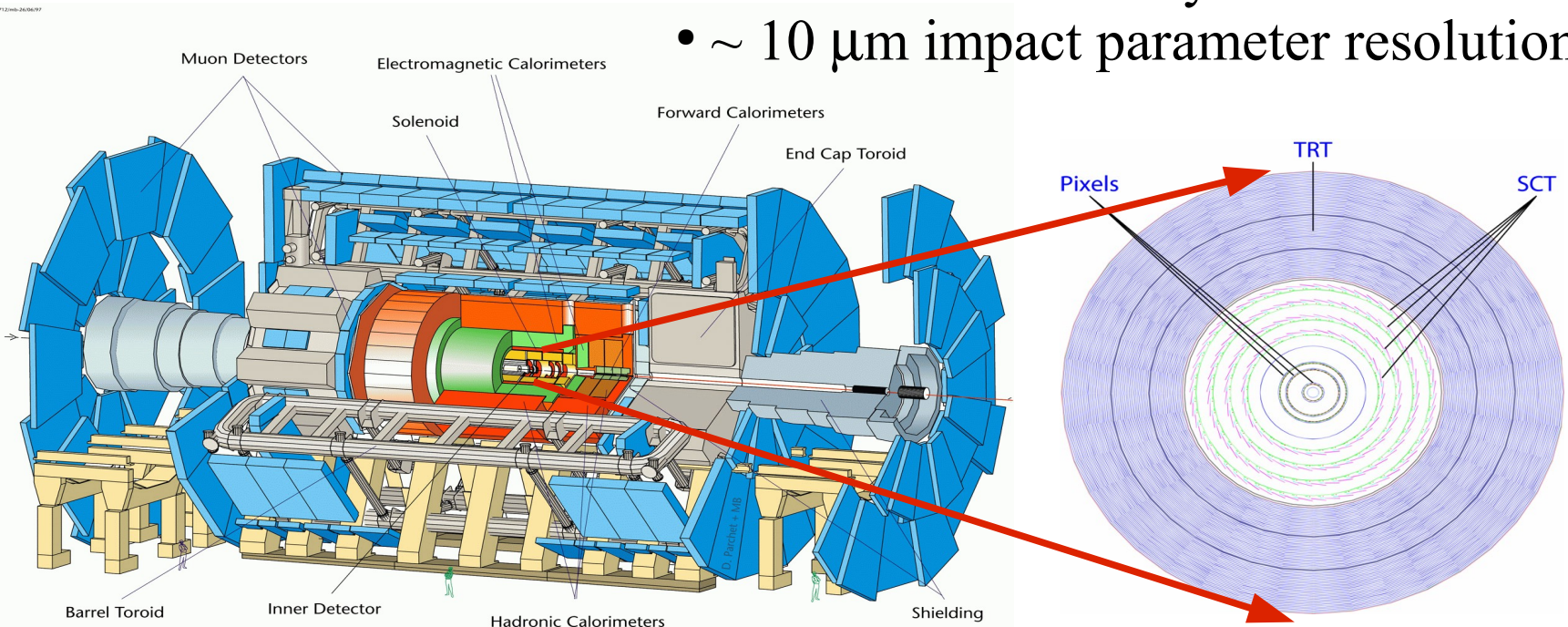




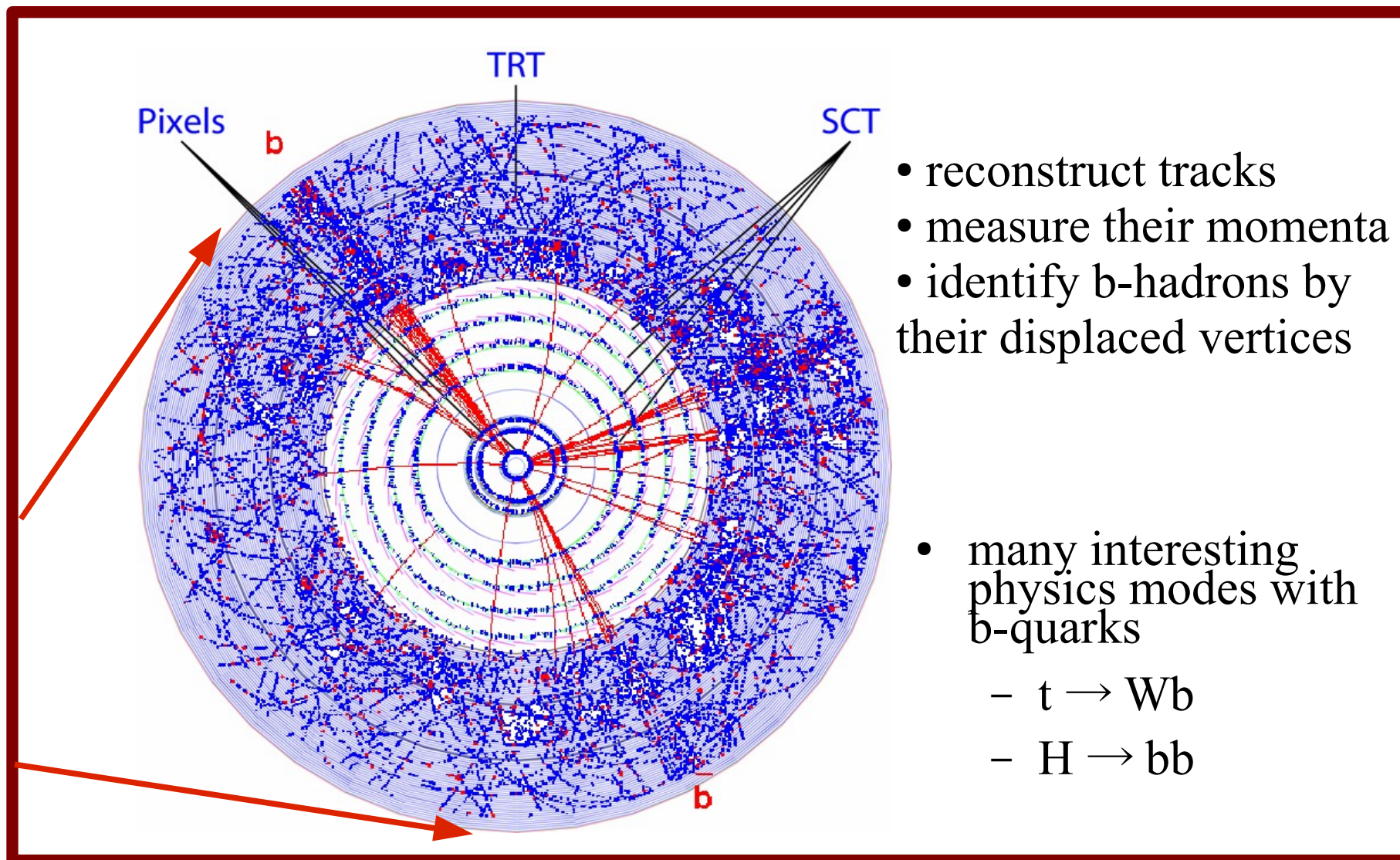
ATLAS Inner Detector

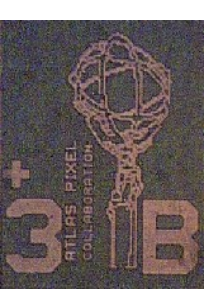
- 1.2m radius, 7m long
- coverage to $|\eta| \leq 2.5$
- inside 2T magnetic field
- 40MHz bunch crossing

- Transition Radiation Tracker (TRT) : 36 space points
- SemiConductor Tracker (SCT): 8 space points
- Pixel Detector: 3 Layers
- $\sim 10 \mu\text{m}$ impact parameter resolution

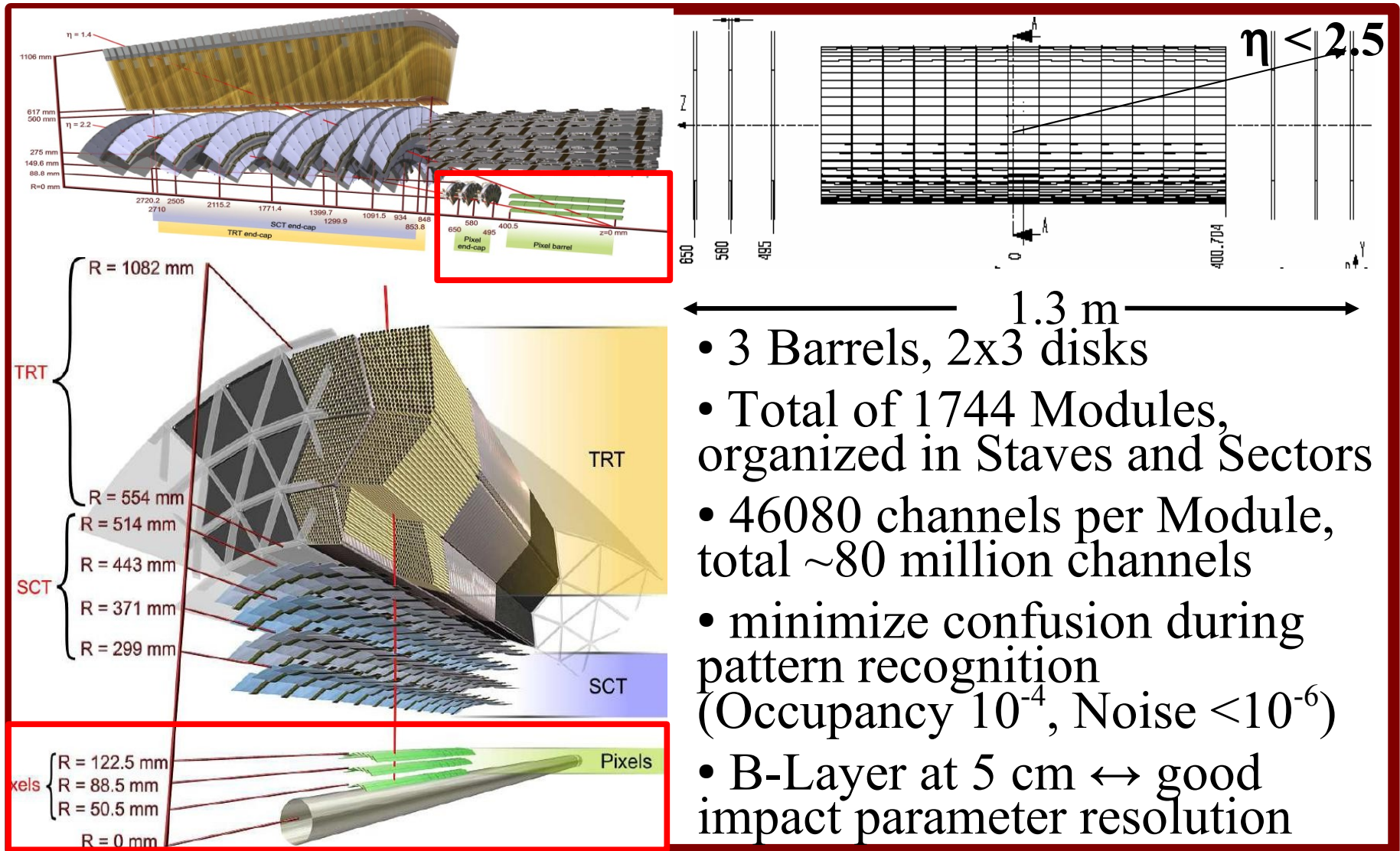


Physics Motivation



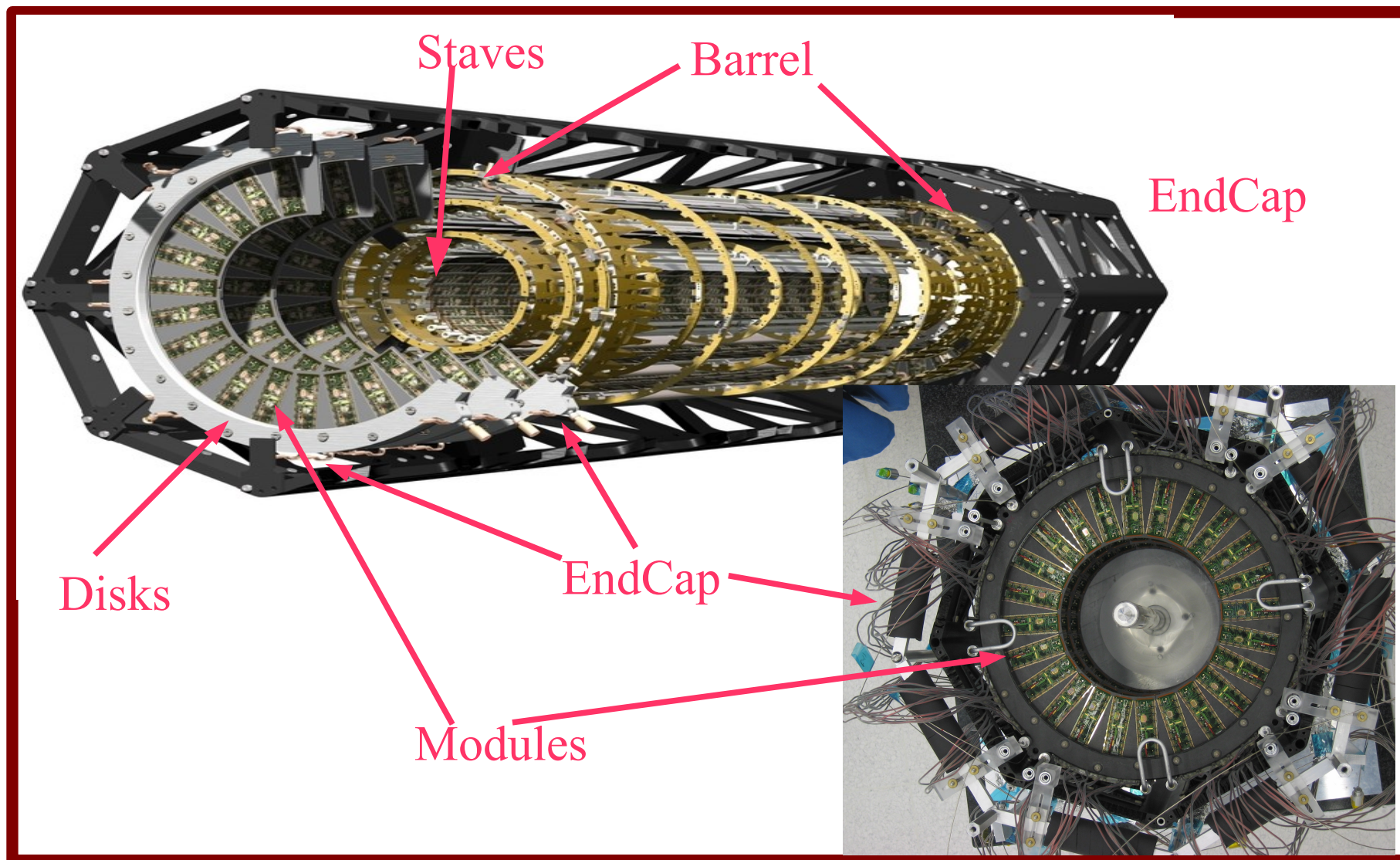


ATLAS Pixel Detector Overview



- 3 Barrels, 2x3 disks
- Total of 1744 Modules, organized in Staves and Sectors
- 46080 channels per Module, total ~ 80 million channels
- minimize confusion during pattern recognition (Occupancy 10^{-4} , Noise $< 10^{-6}$)
- B-Layer at 5 cm \leftrightarrow good impact parameter resolution

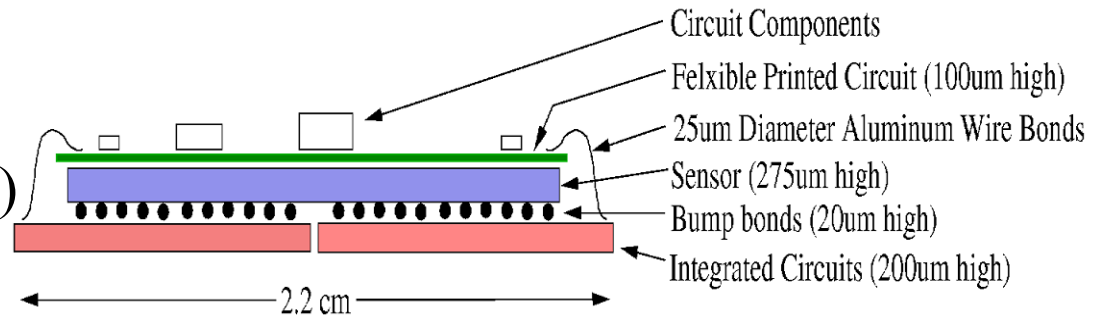
Pixel Overview





Pixel Module

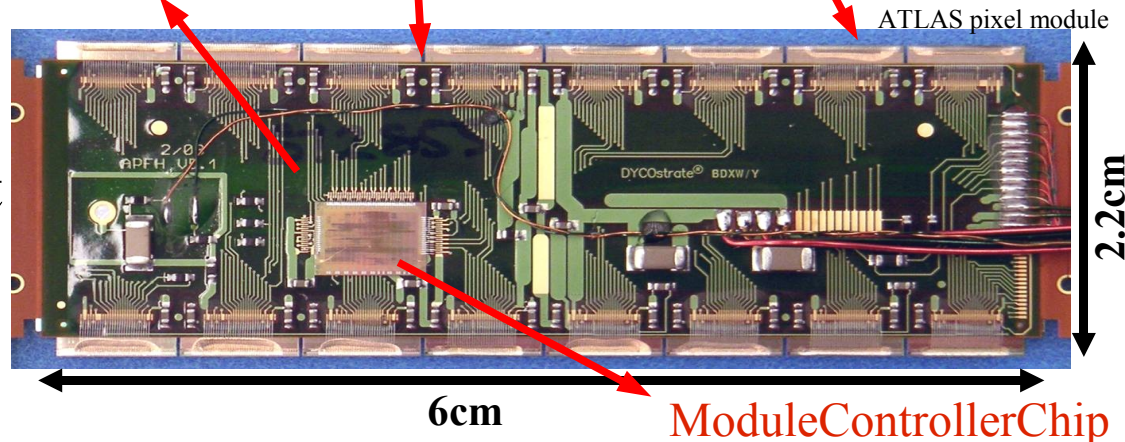
- radiation hard to 50MRad (NIEL > 10^{15} 1 MeV n_{eq}/cm^2)
- fast readout (25ns beam crossing, 3.2 μ s latency)
- Hybrid chip technology
- 0.25 μ m CMOS FrontEnd
- 1 Sensor bump bonded to 16 FrontEnd chips
- FrontEnd chips connect to ModuleControllerChip
- Zero suppression and first event building



FlexPCB

Sensor (below)

FE-ICs

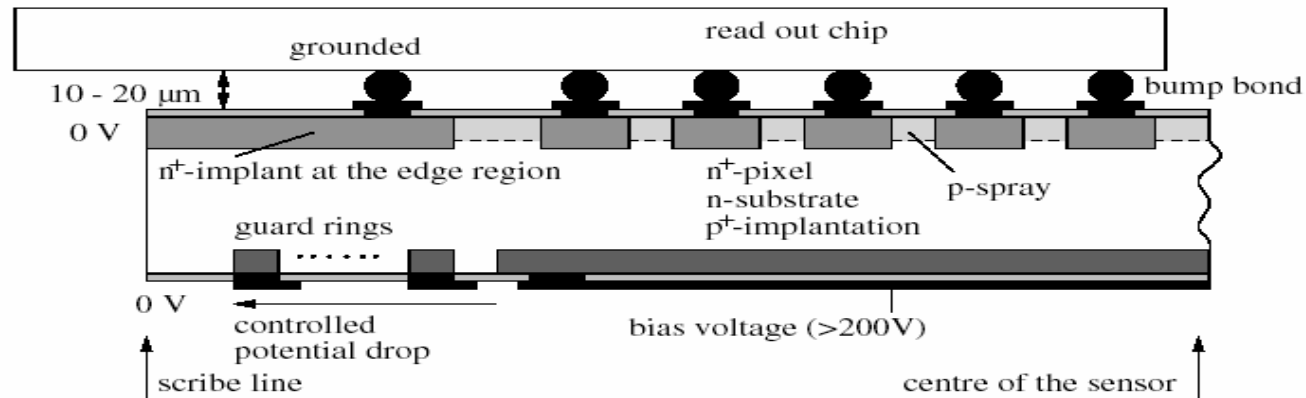
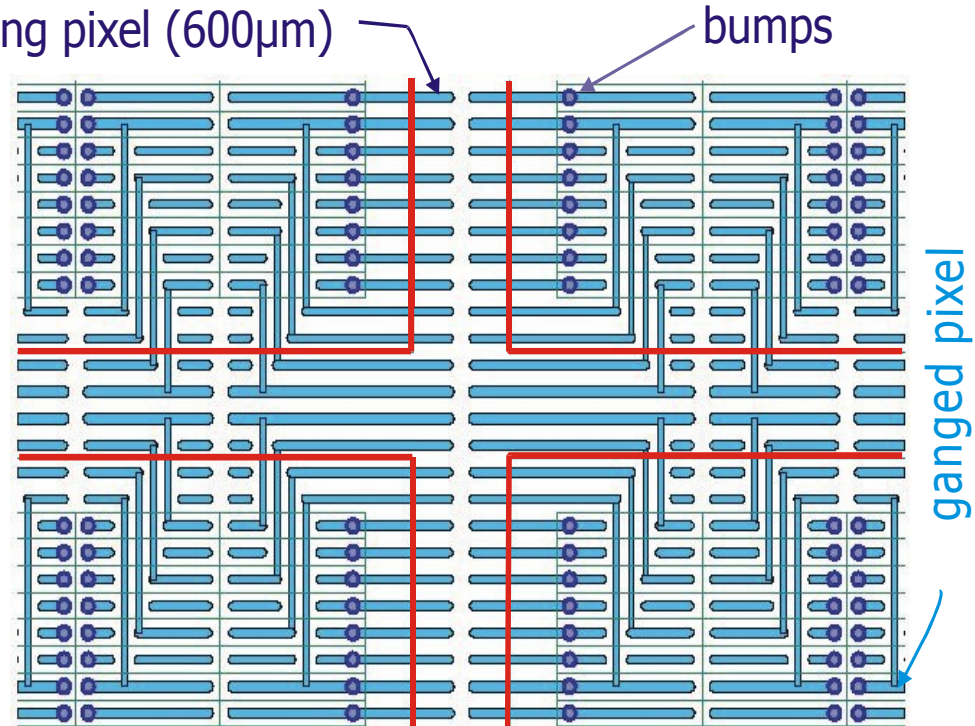


6cm

ModuleControllerChip

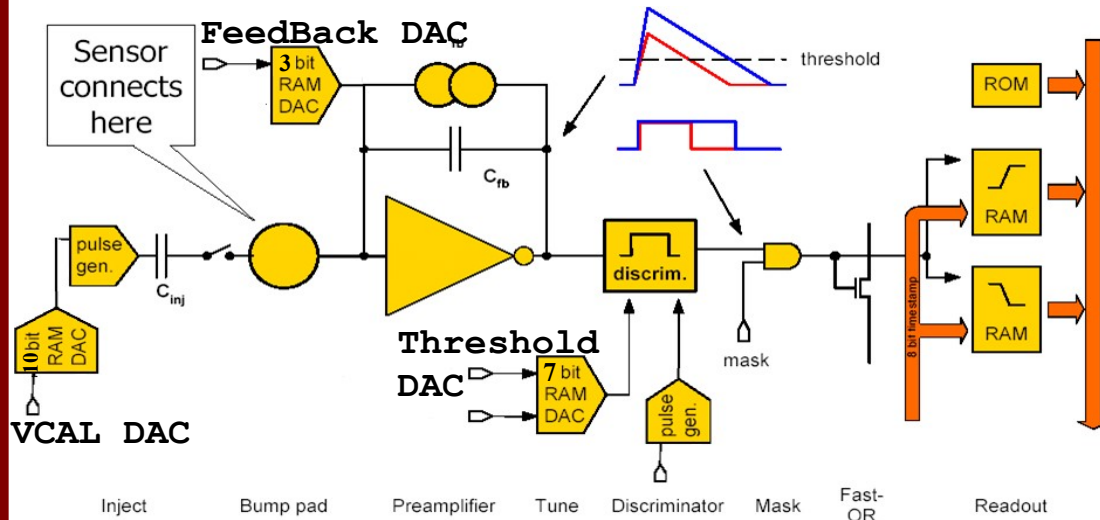
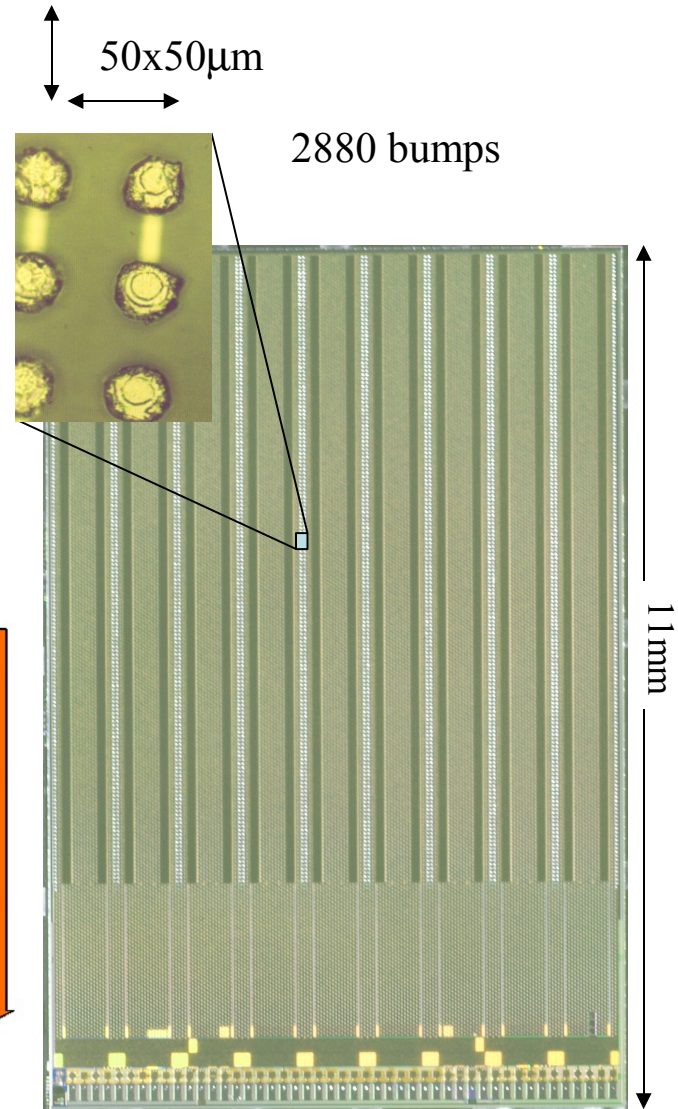
Pixel Sensor

- n-substrate
- active area $60.8 \times 16.2 \text{ mm}^2$
- total area $\sim 1.8 \text{ m}^2$
- 41984 $50\mu\text{m} \times 400\mu\text{m}$ Pixels
- special Pixels for chip overlapp regions:
 - 5284 “long” $50\mu\text{m} \times 600\mu\text{m}$
- after radiation:
 - 97.8% eff
 - $9.7 \mu\text{m}$ $R\phi$ resolution



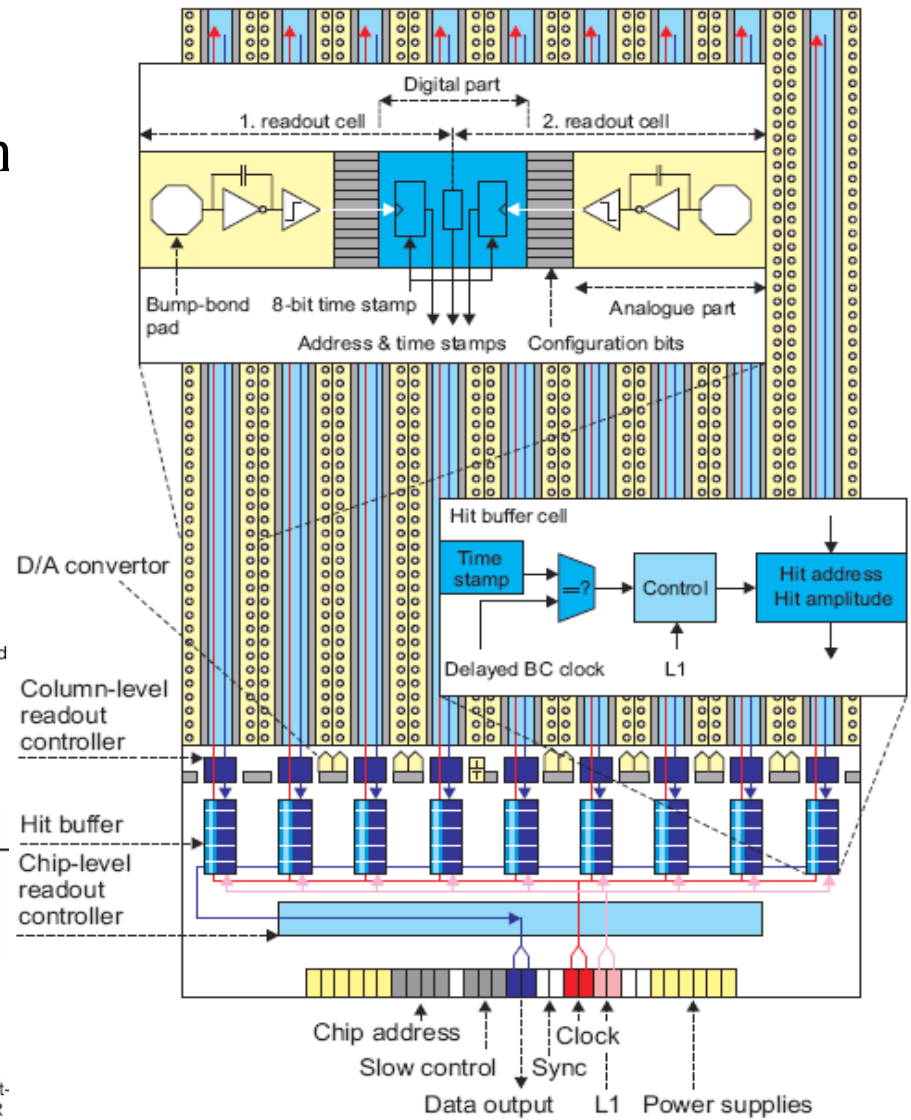
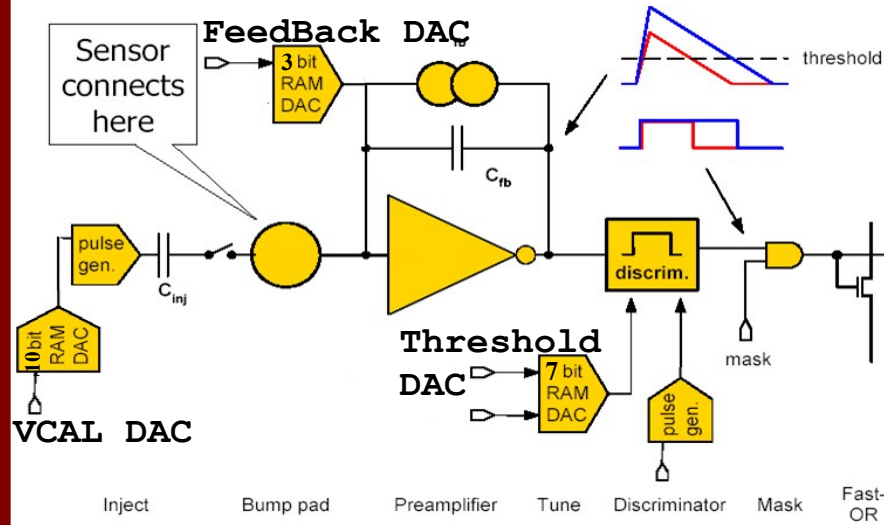
Pixel FrontEnd Chips

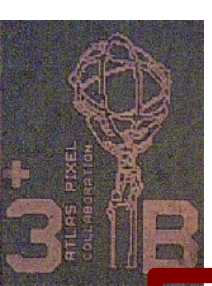
- 2880 channels organized in 18 columns and 160 rows
- connected to sensor via 50 μm pitch bump bonds
- buffering + time stamp logic
- converts charge into Time over Threshold (ToT)
- charge injection
- detailed monitoring



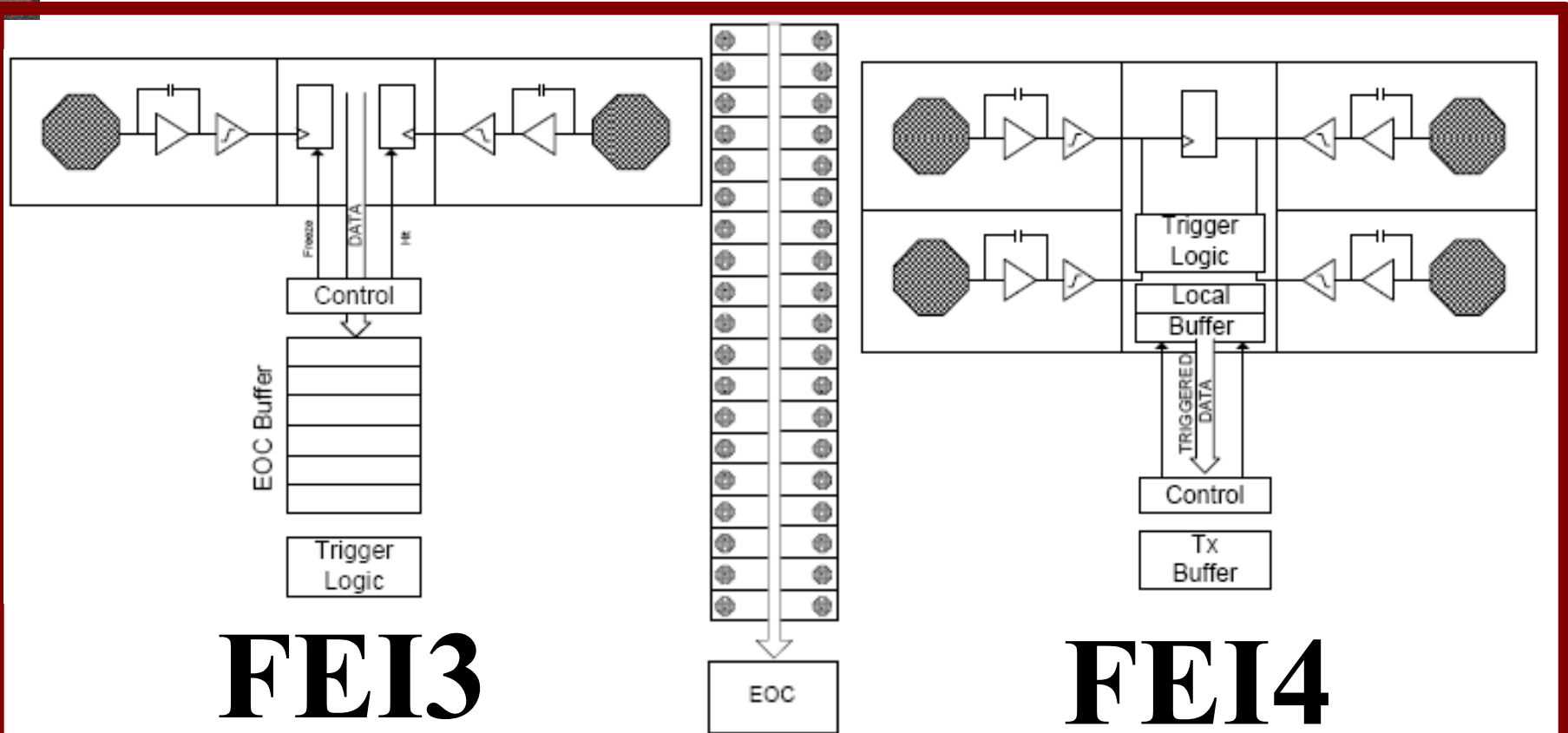
Pixel FrontEnd Chips

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Upgrade: Pixel FrontEnd Chip FEI4

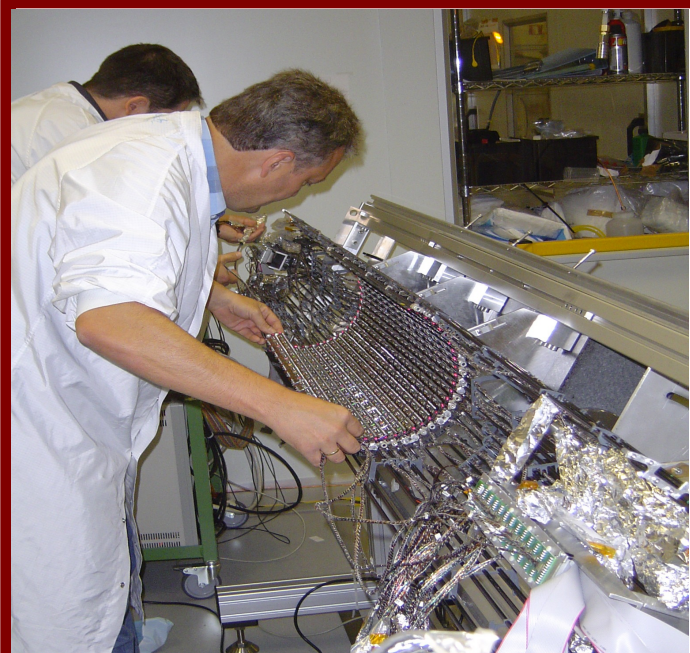


FEI3

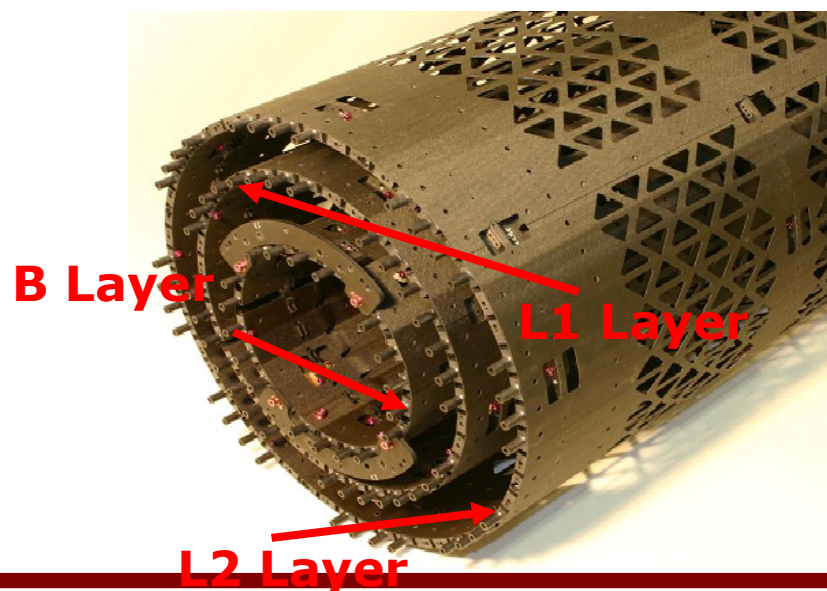
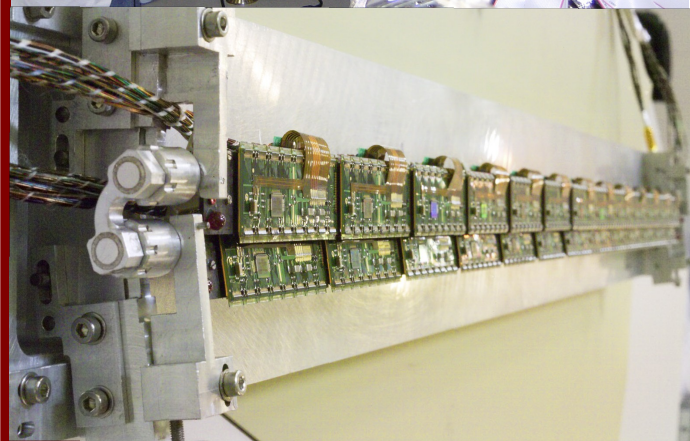
FEI4

- FEI3 design has limitations at high occupancy
- Possible solution in FEI4:
 - local storage + readout only triggered hits
- New concept: cluster timing (larger hits have better timing)

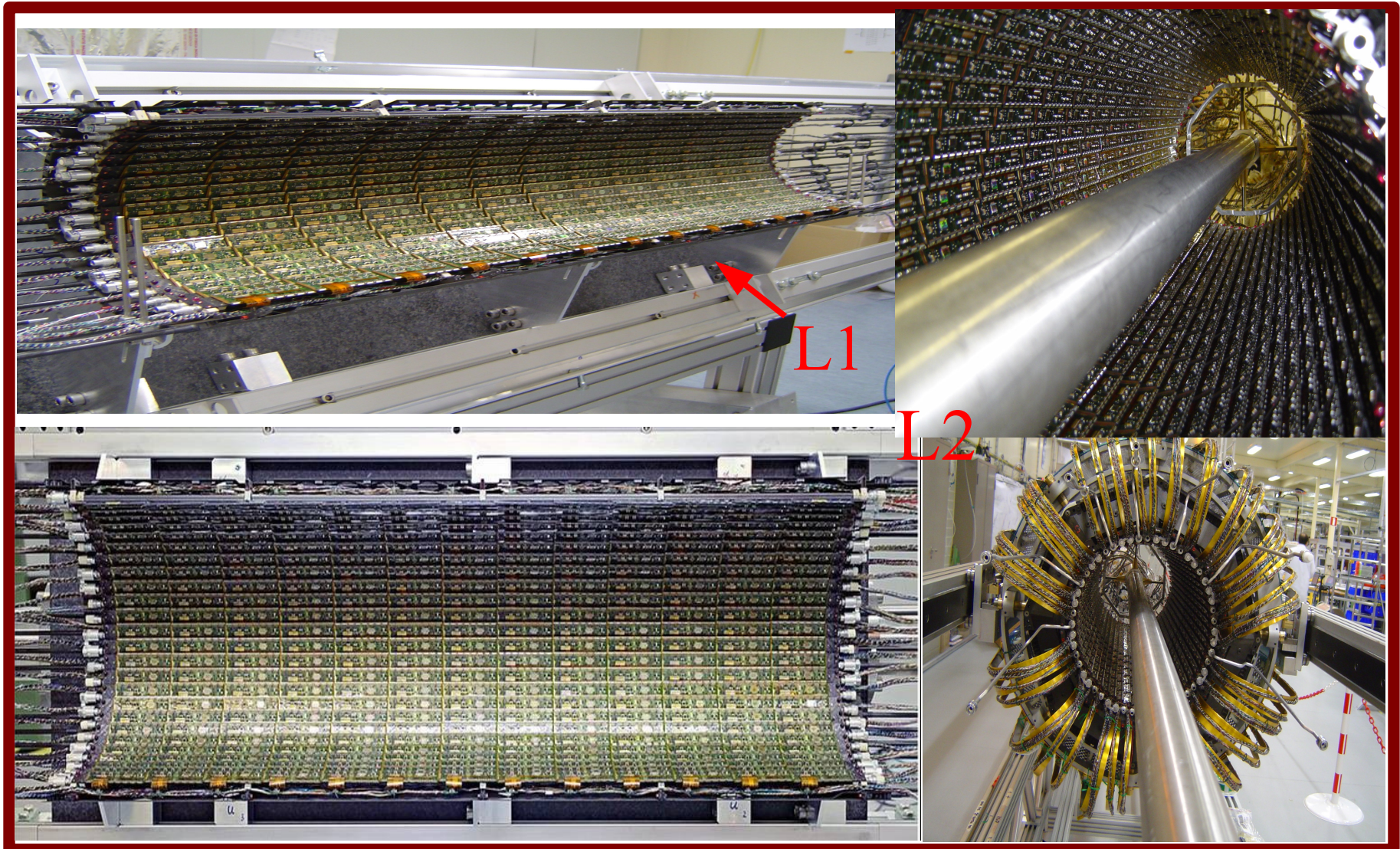
Pixel Barrel



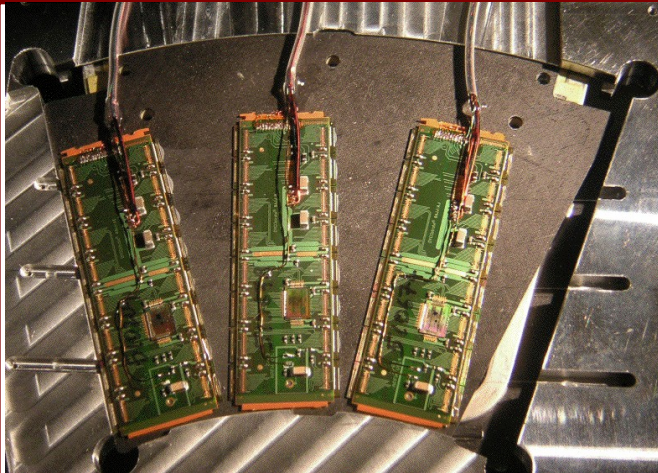
- carbon-carbon support ladder
- integrated cooling pipe
- 13 modules per stave
- bi-staves with cooling U-Link are inserted into half-shells
- finally clamping of half-shells



Barrels: L2, L1



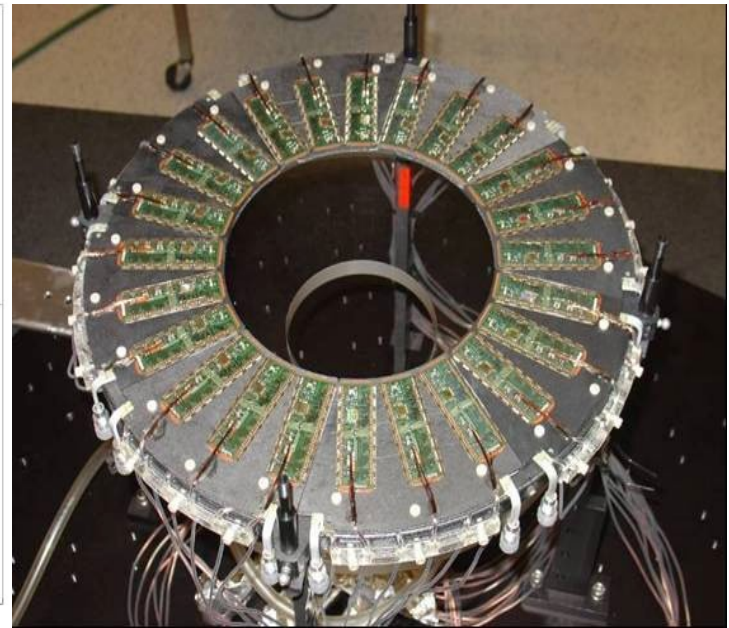
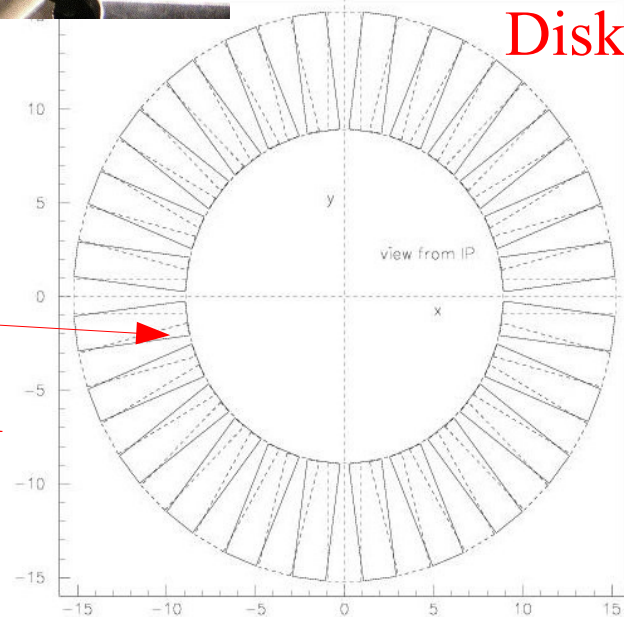
Pixel EndCaps



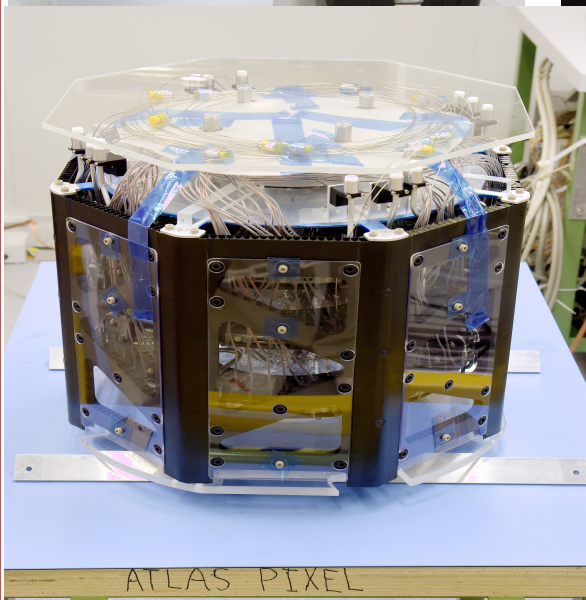
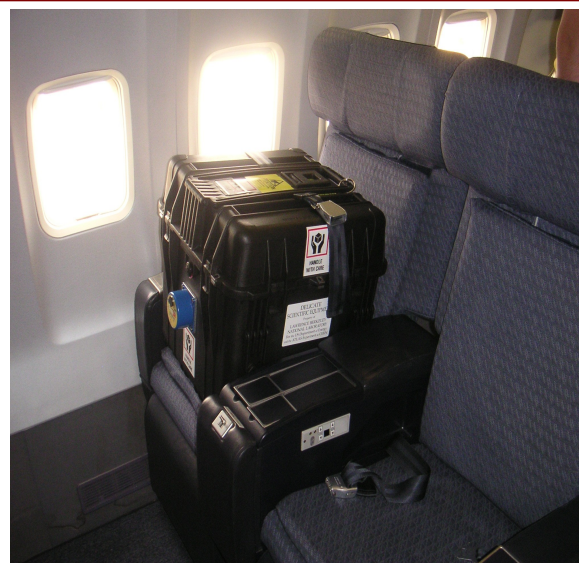
- 3 modules on each side of a sector, 6 modules total
- cooling pipe sandwiched between carbon-carbon plates
- 8 sectors to form a disk
- 3 disks per EndCap

Sector

Overlapp
region between
modules

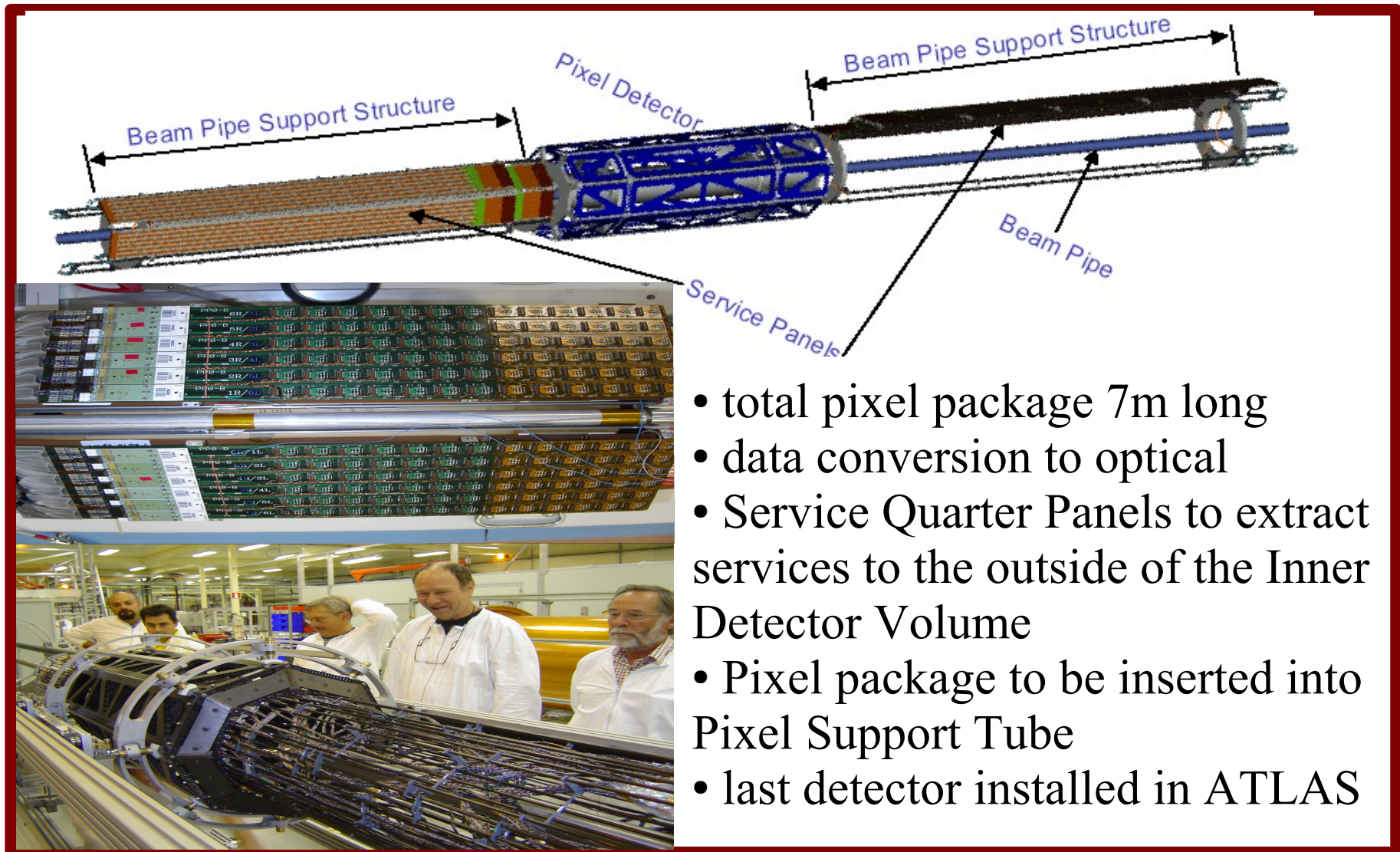


How to Transport an EndCap

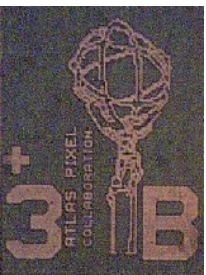


- transported from Berkeley by plane
- both EndCaps delivered safely to CERN
- underwent detailed reception testing
- $< 0.2\%$ dead pixels in EndCaps!

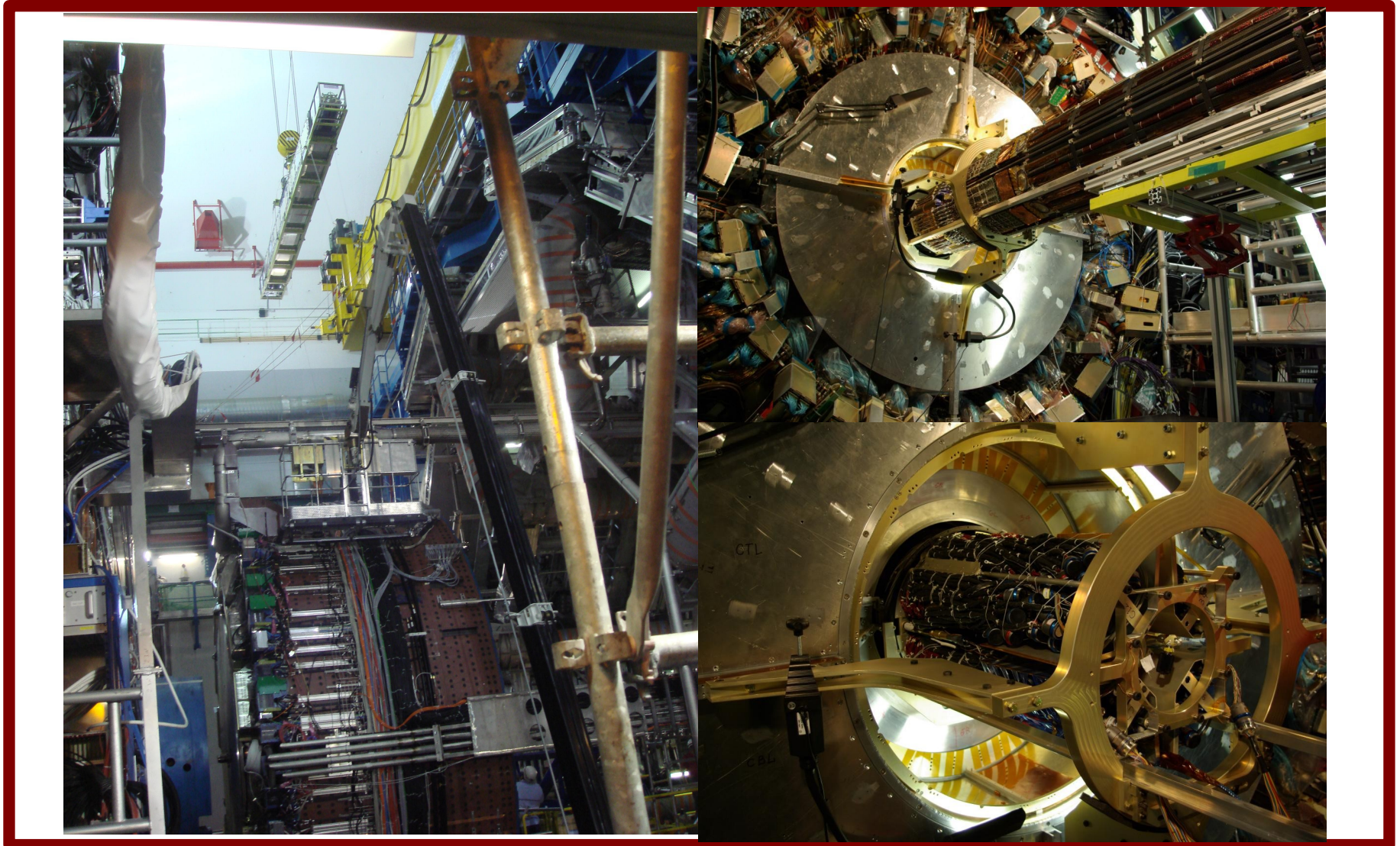
PixelPackage

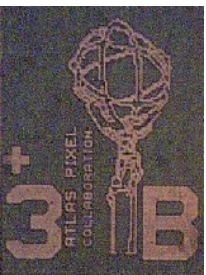


- total pixel package 7m long
- data conversion to optical
- Service Quarter Panels to extract services to the outside of the Inner Detector Volume
- Pixel package to be inserted into Pixel Support Tube
- last detector installed in ATLAS



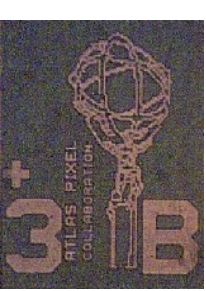
Pixel Insertion





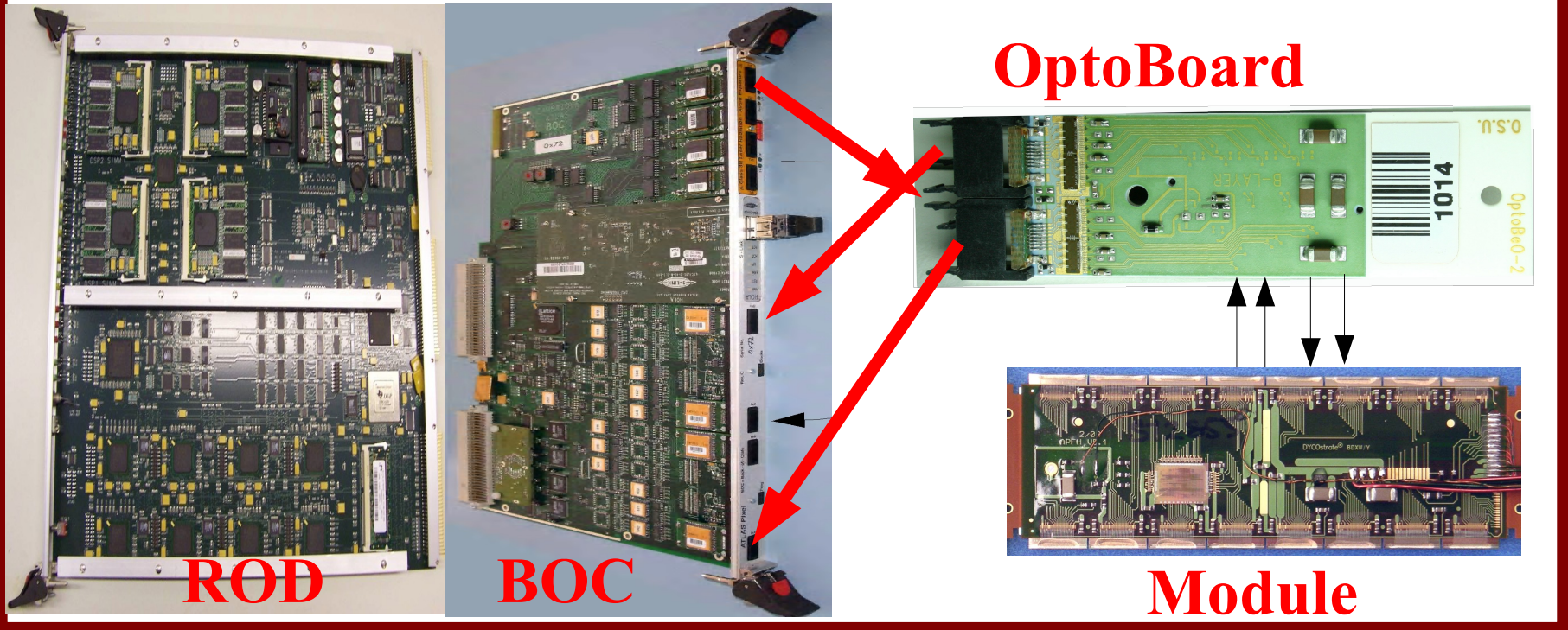
Pixel Connection

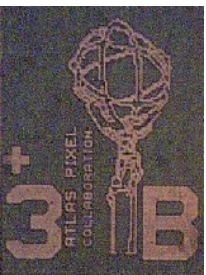




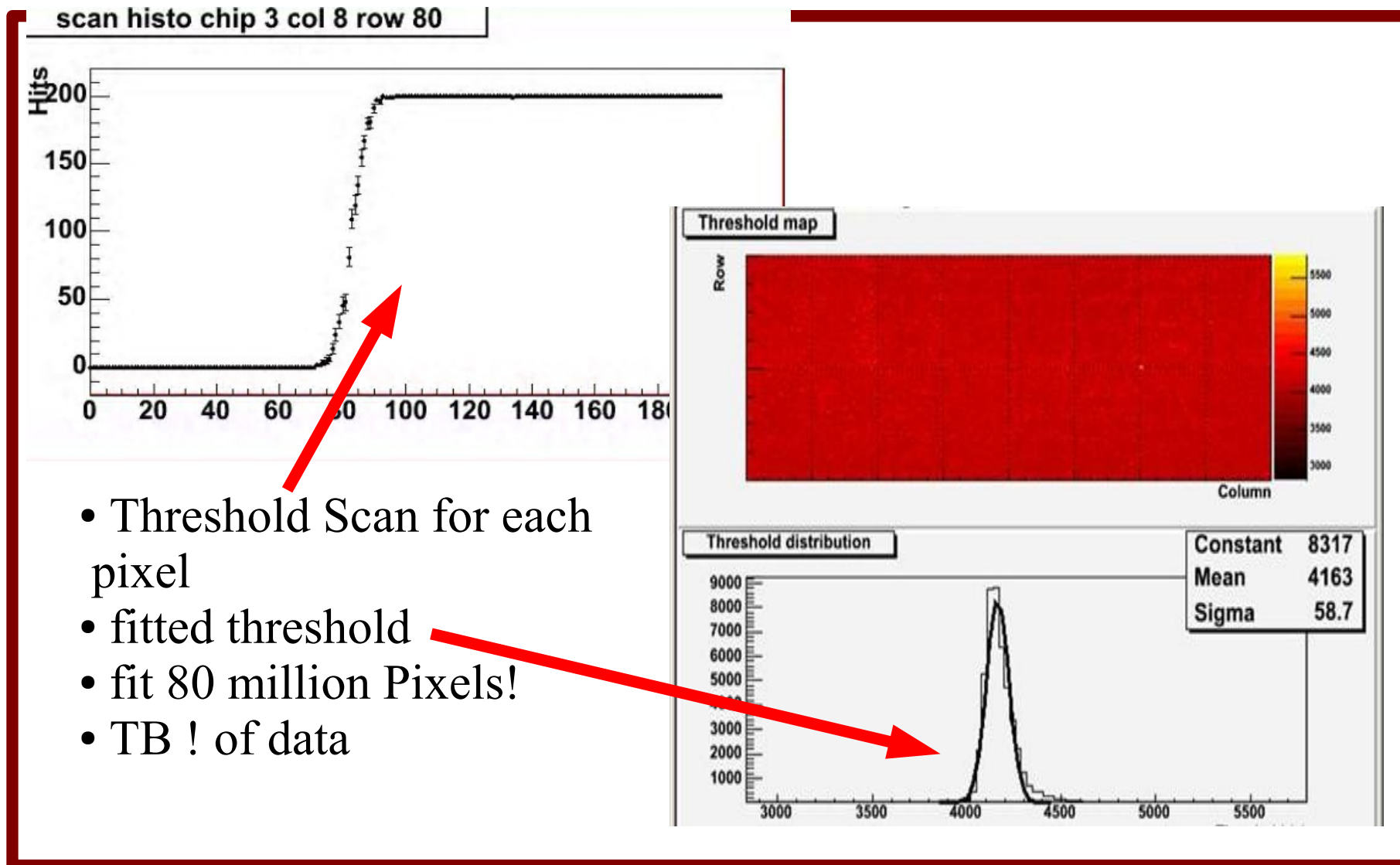
Pixel ReadOut

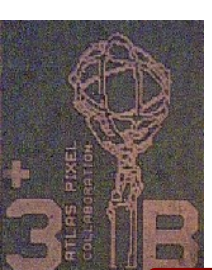
- Module can send data with 40(L2), 80(L1, Disk) or 160 Mbit(B)
- 4 LVDS connections to Module: Clock, DataIn, 2 x DataOut
- optoBoard services 6/7 modules, converts electrical to optical
- BackOfCrate card encodes clock/data, decodes in 40MHz streams
- ReadOutDrive with 4+1 DigitalSignalProcessors and 1GB RAM



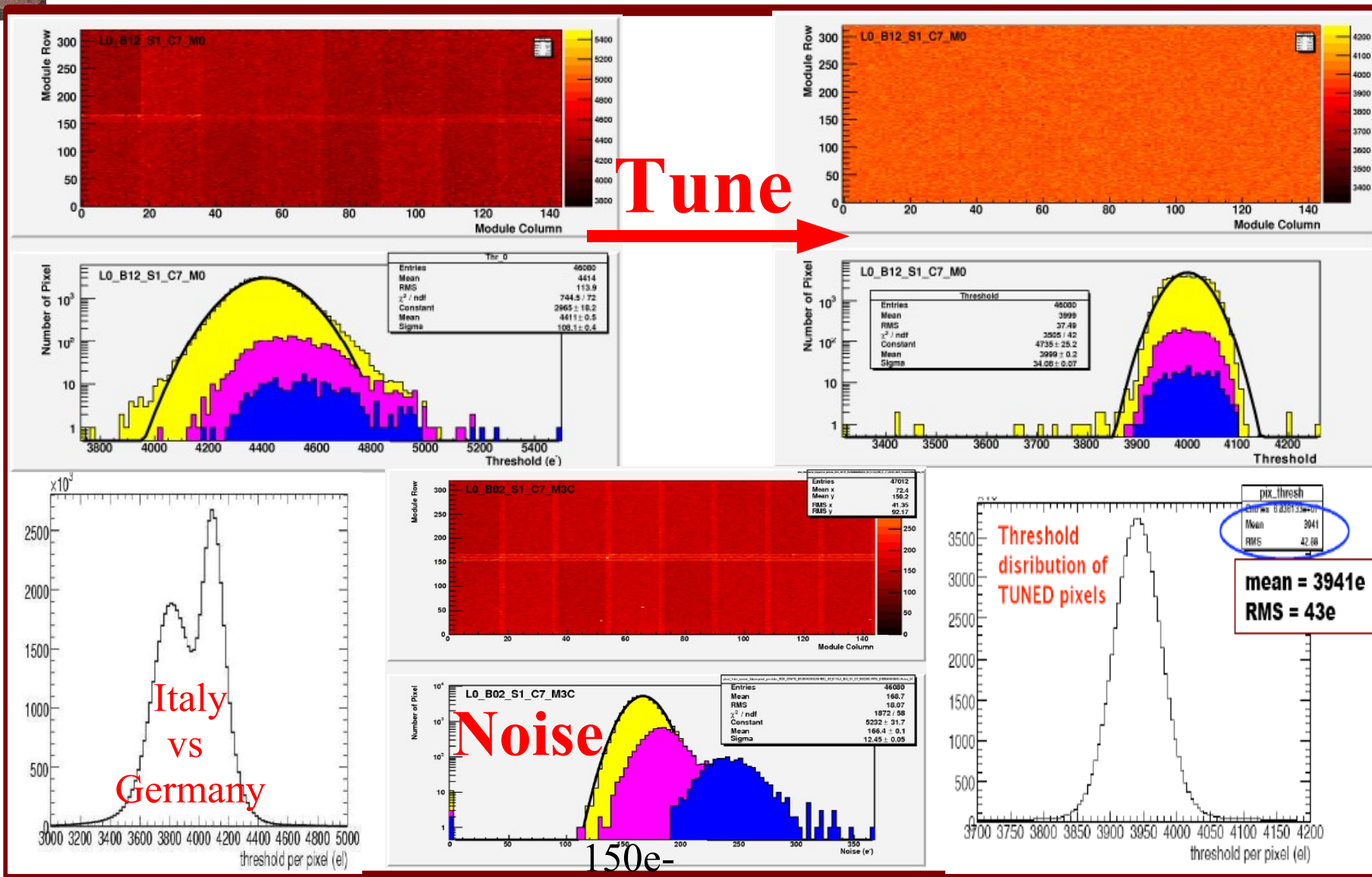


Calibration Challenges



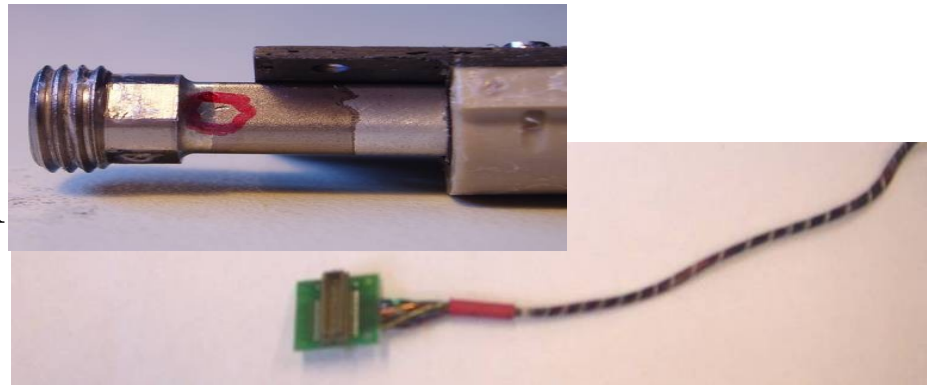


Threshold & Noise



Service Challenges and Problems

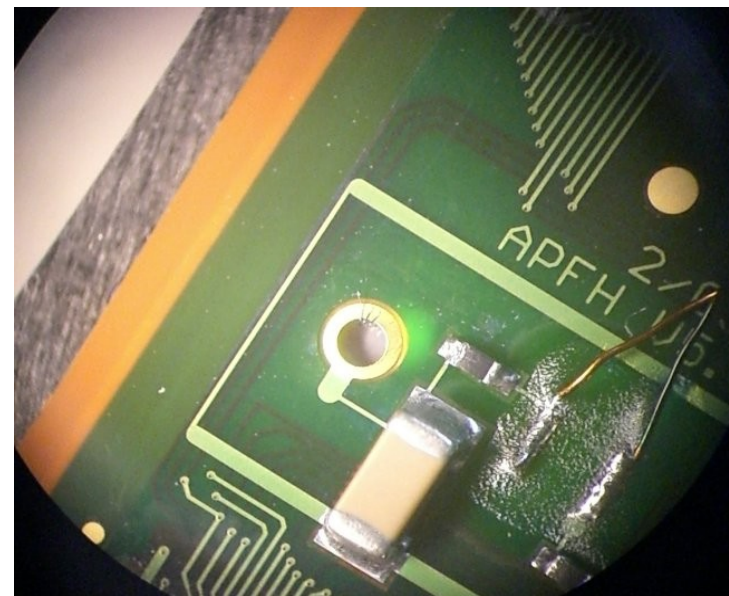
- Service Challenges:
 - power: operation at $\sim 2\text{V}$, 1A over 100m cables require additional radiation hard voltage regulators close to the detector
 - 6kW of heat removed with C3F8 evaporative cooling
- Problems overcome:
 - cooling pipe corrosion
 - breaking cables
- never underestimate the “simple” parts



Problems: HV

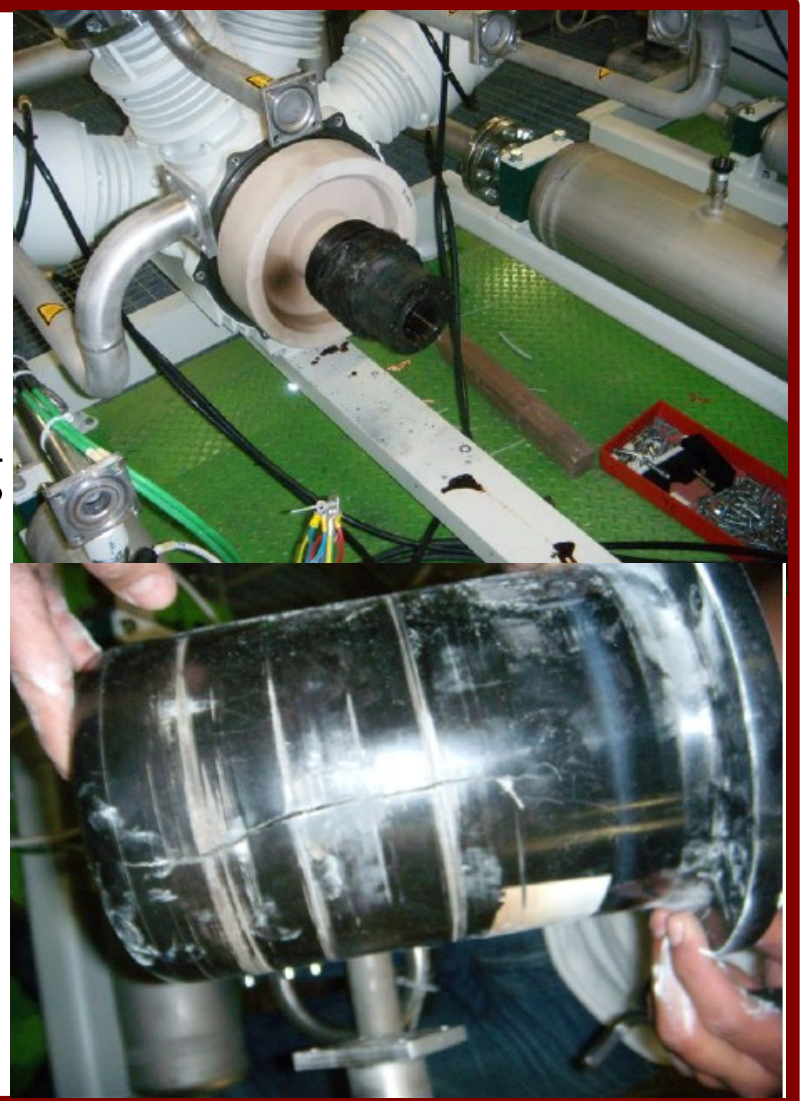
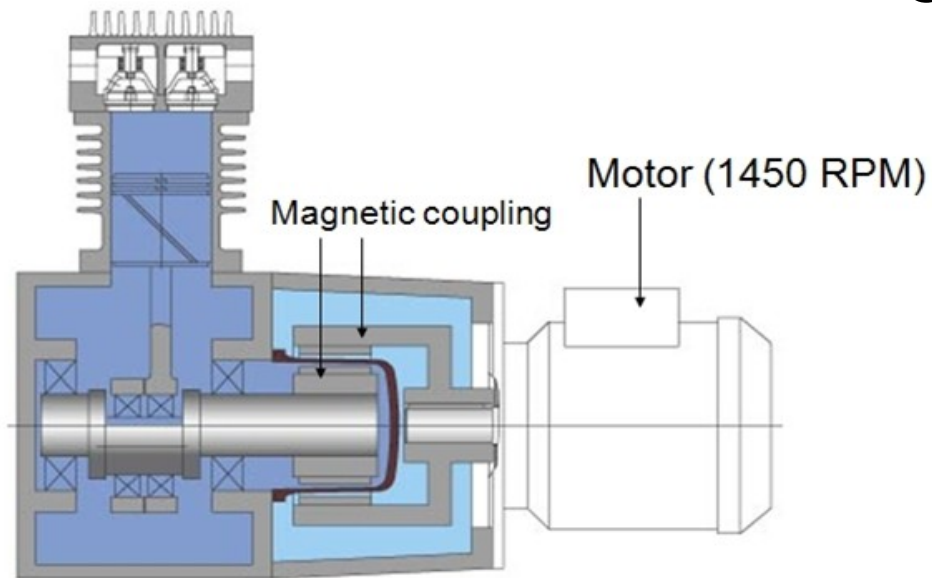
- Problems

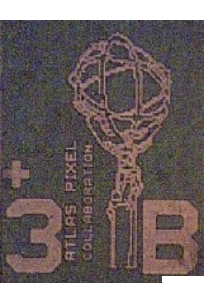
- HV opens are the largest source of irrecoverably lost modules (11)
- There are three different sources
 - Broken wire bonds (likely due to flex bow)
 - Broken cables
 - Open module connectors
- Some might have slipped through a fault in the connection test



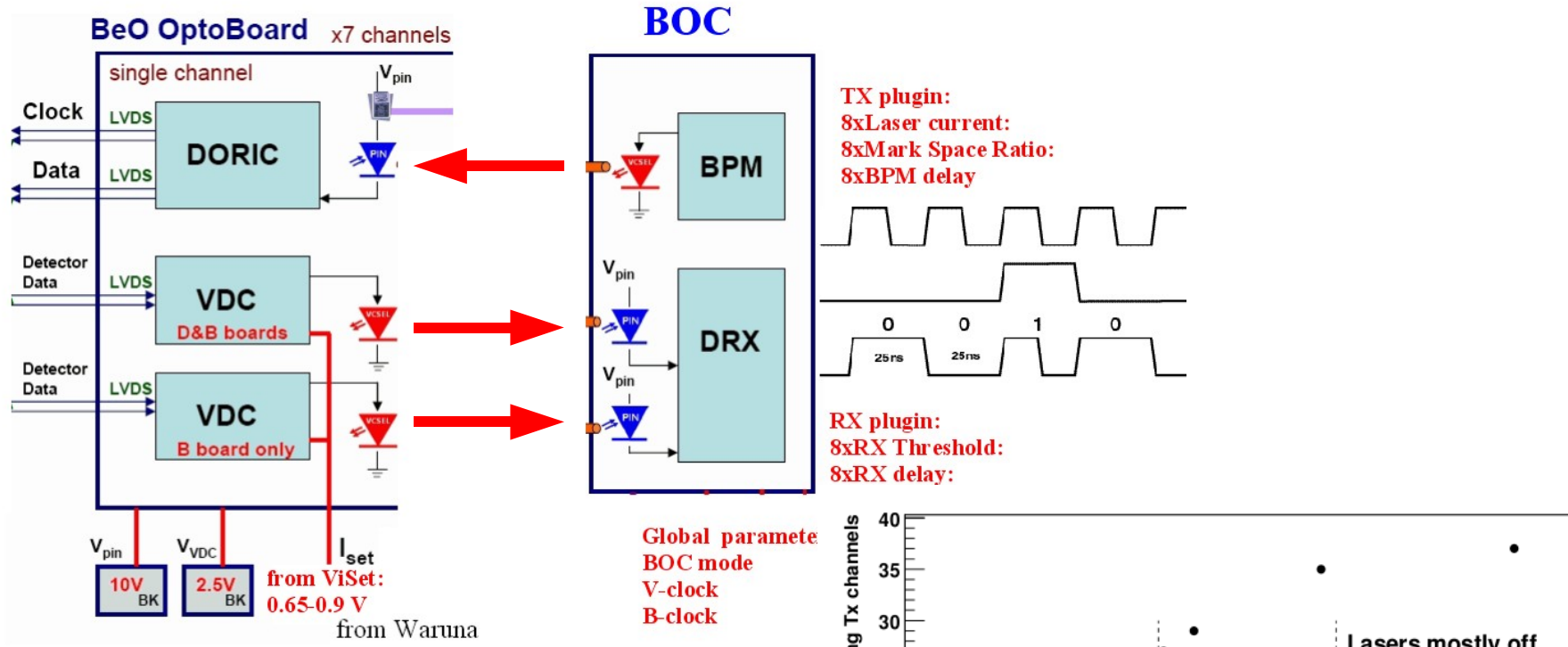
Problems: Cooling

- Incident May 1st
 - caused serious delay
 - 3 compressors damaged
 - Coupler housing cracked
 - Cooling fluid contaminated
 - Showed a lack of monitoring

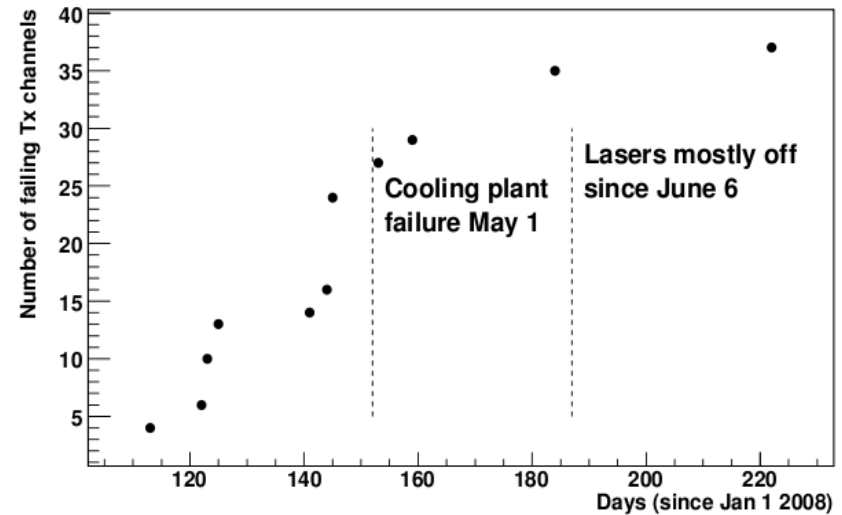




Problems: Optical Transmission

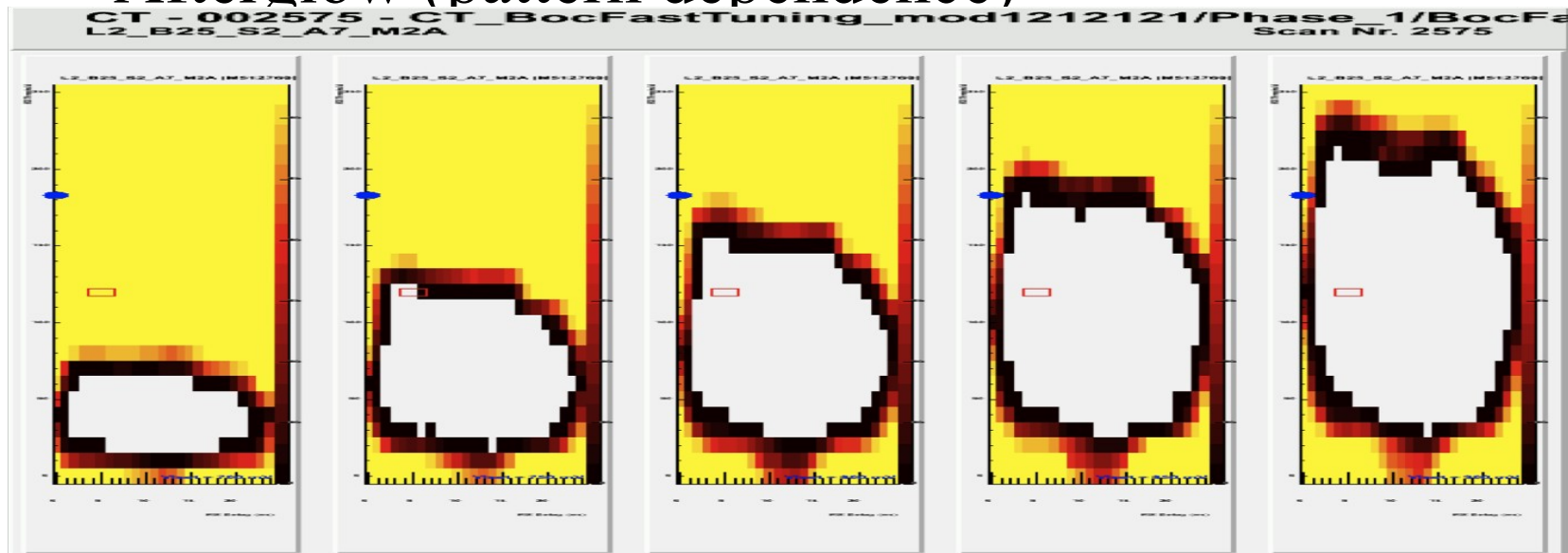


- Transmission towards modules very stable, but observe dying channels due to ESD



Problems: Optical Transmission

- Data transmission to the off detector electronics
 - Opto board operating temperature (20C, now use heaters)
 - Only one parameter for 7 channels (ViSet)
 - Slow turn on
 - Afterglow (pattern dependence)



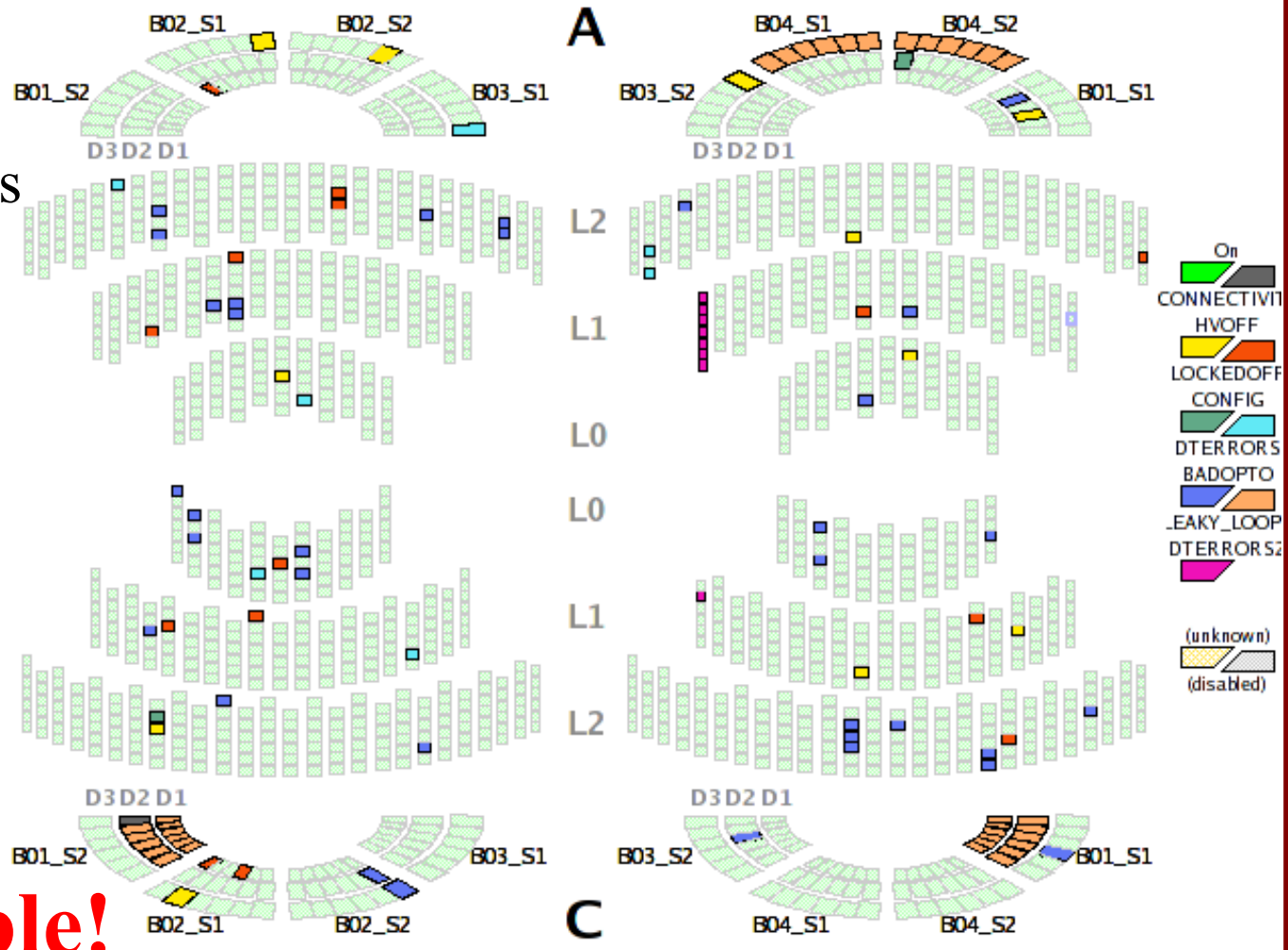


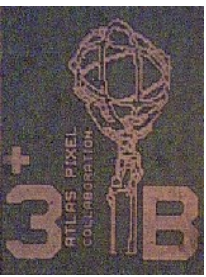
Pixel Status

Disabled modules

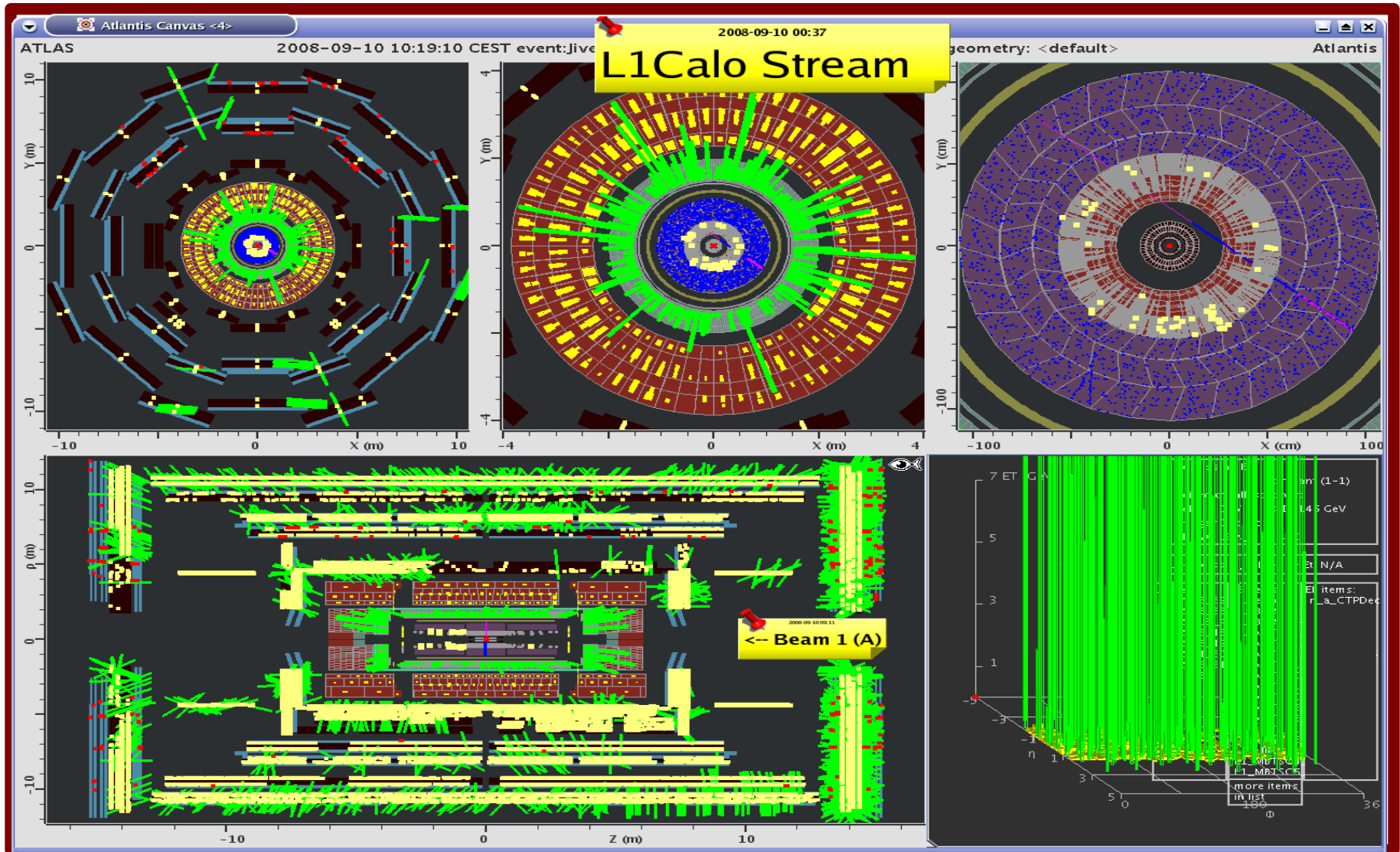
Leaky Loops	36
Bad Opto	35
HV Open	11
BAD TX	2
Locked Off	14
Data Errors	15

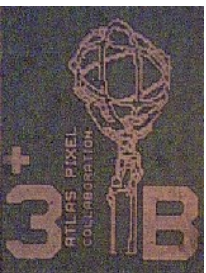
Largely recoverable!





First Beam!

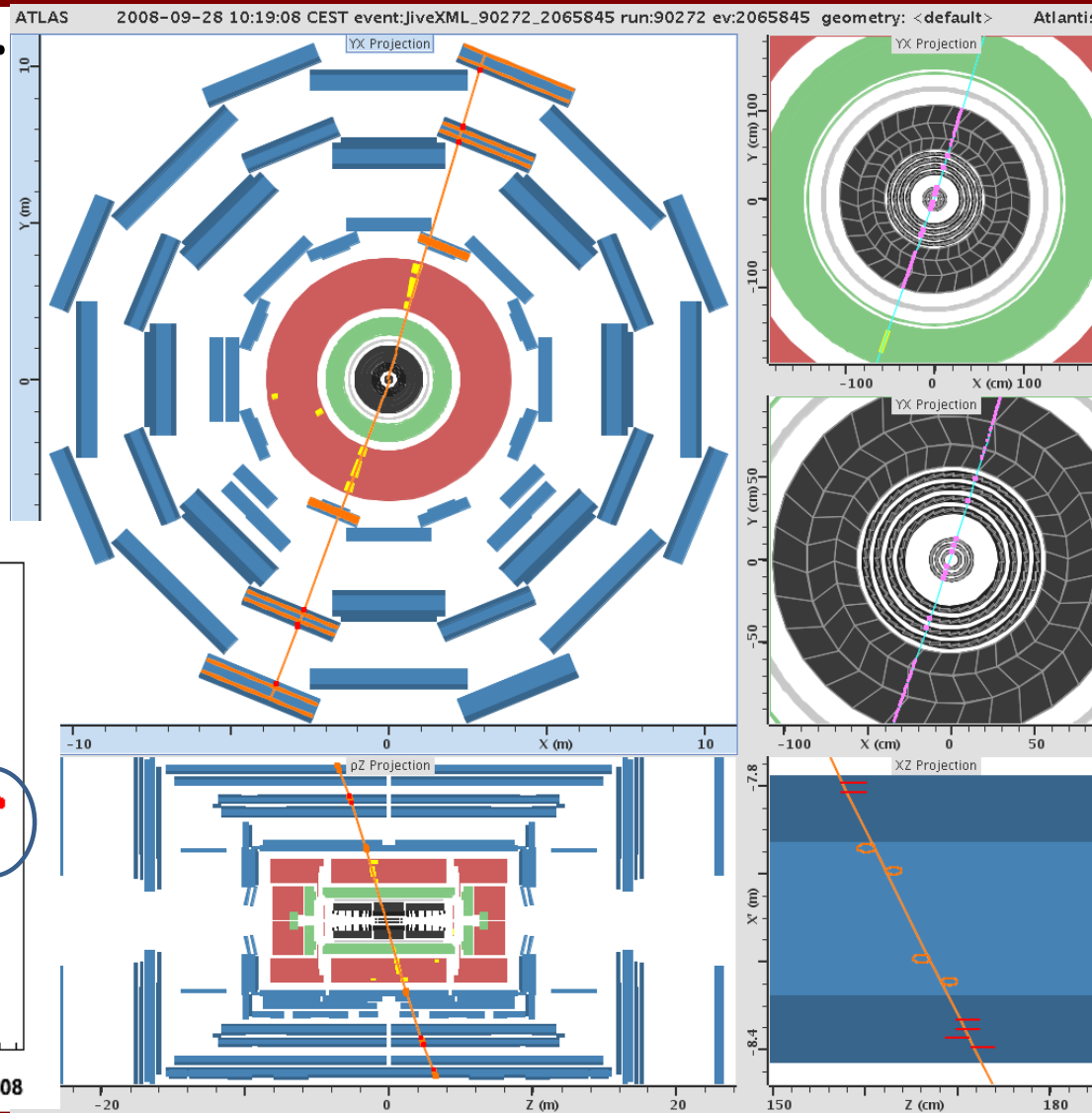
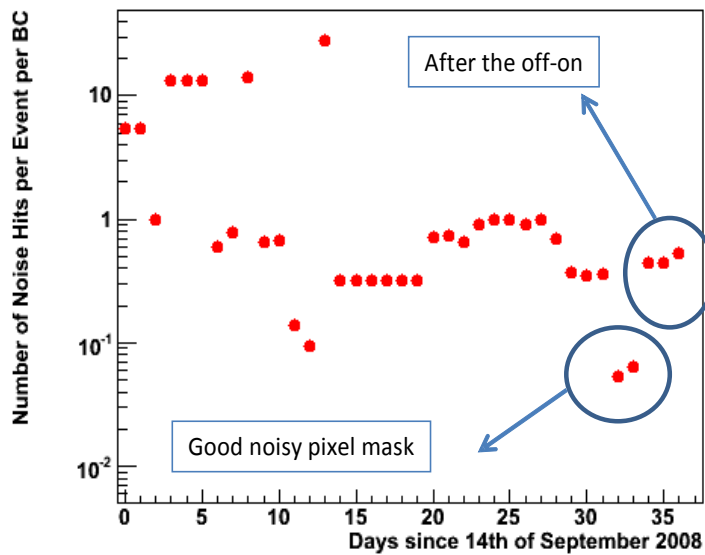


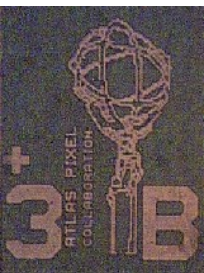


Data: Cosmics!

• Cosmic rays used for

- Noise studies

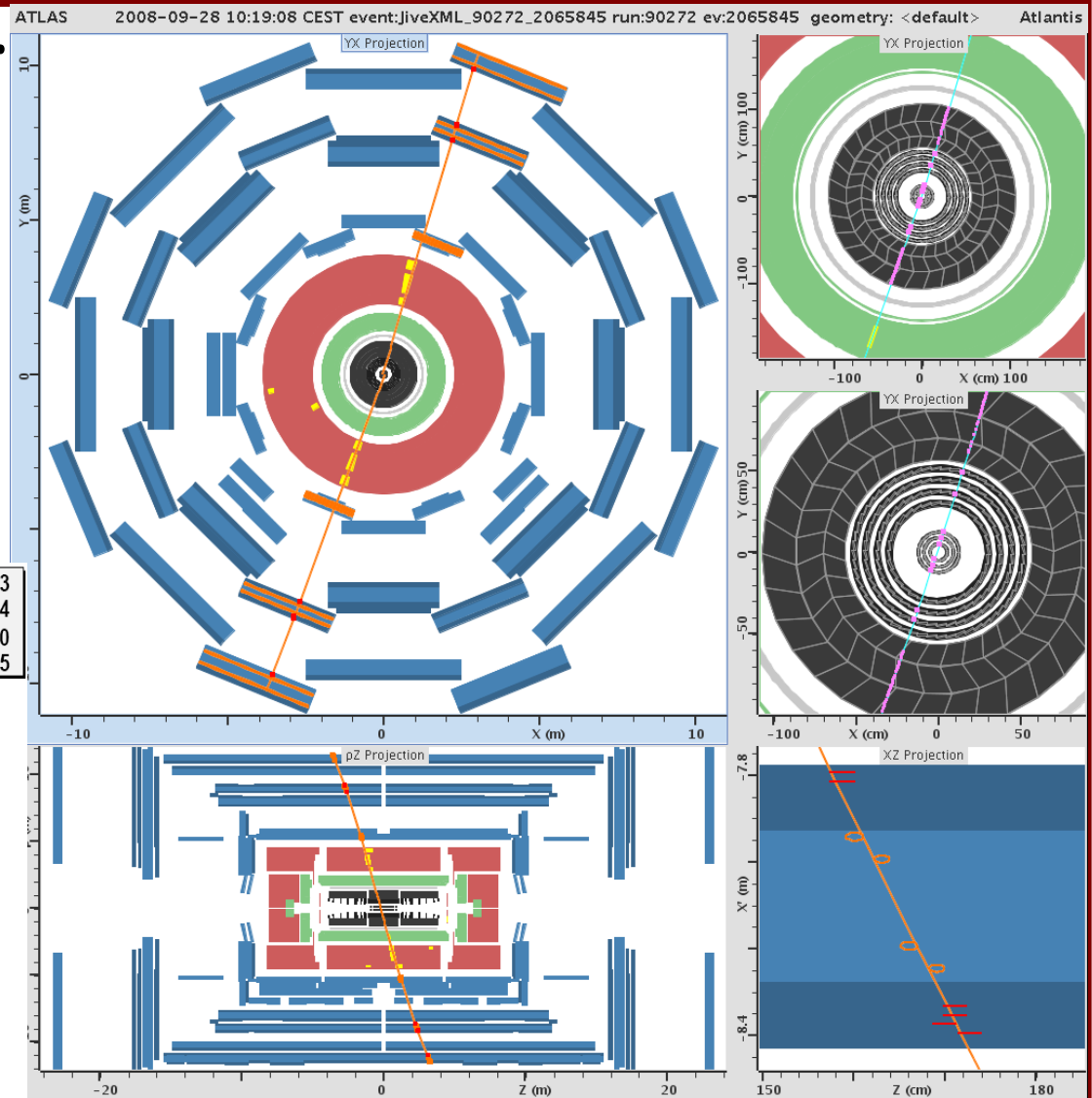
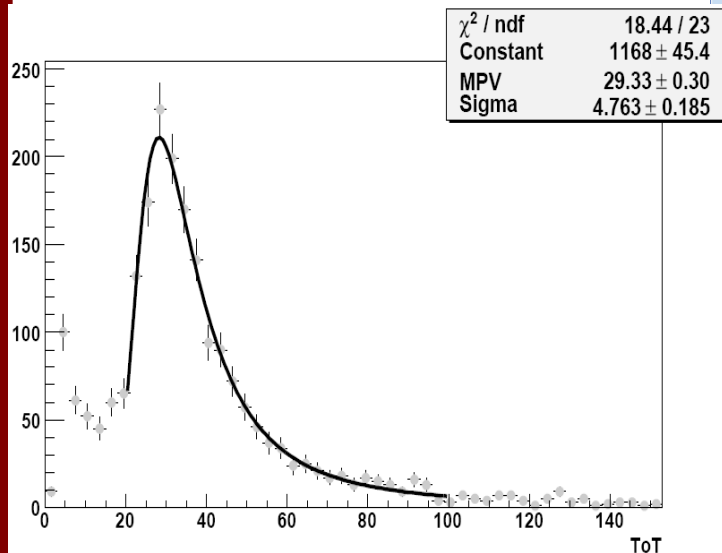


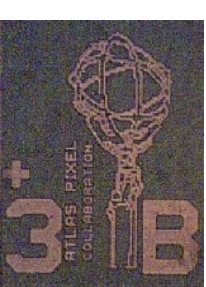


Data: Cosmics!

- Cosmic rays used for

- Noise studies
- Cluster studies
-
-

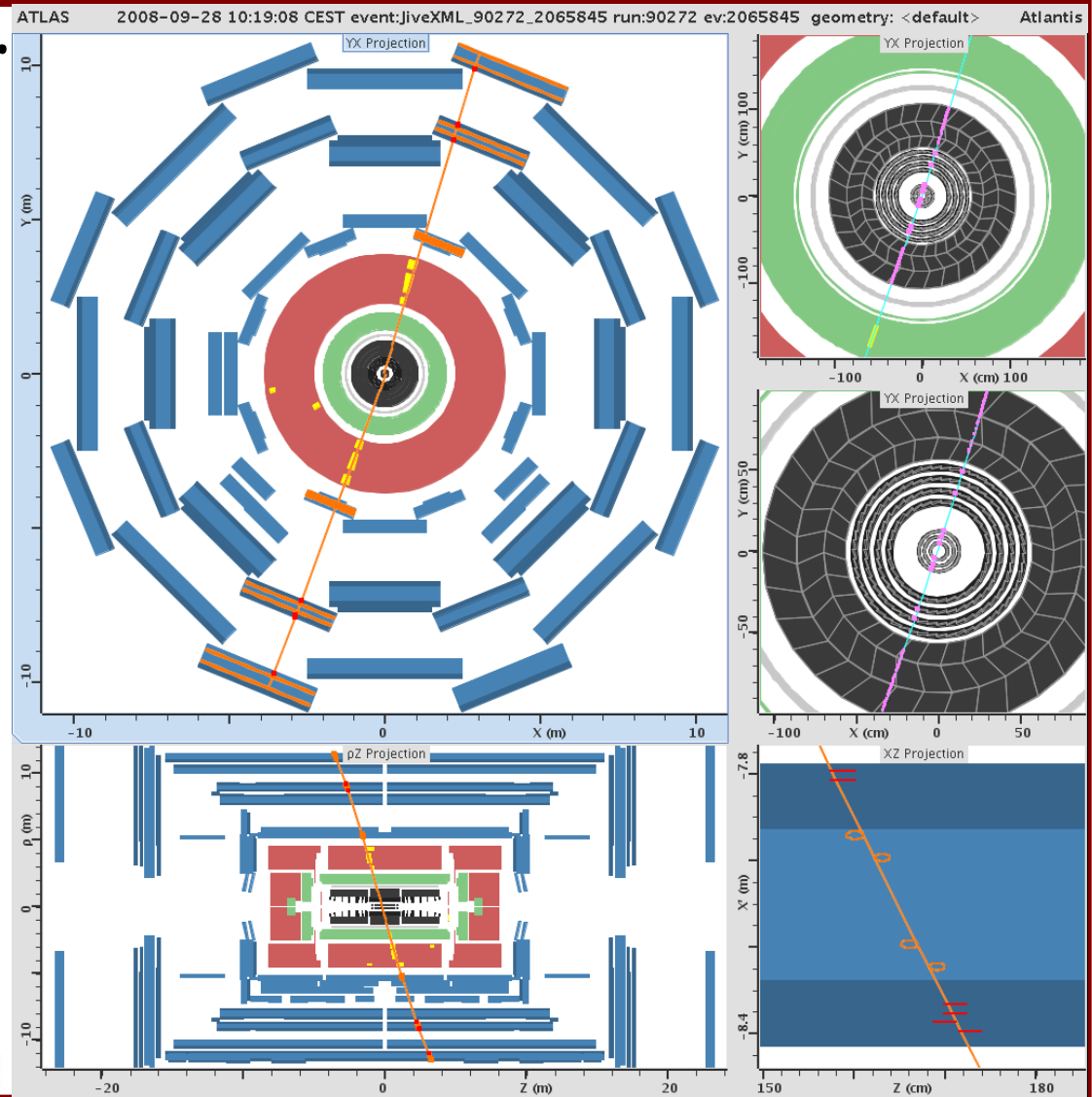
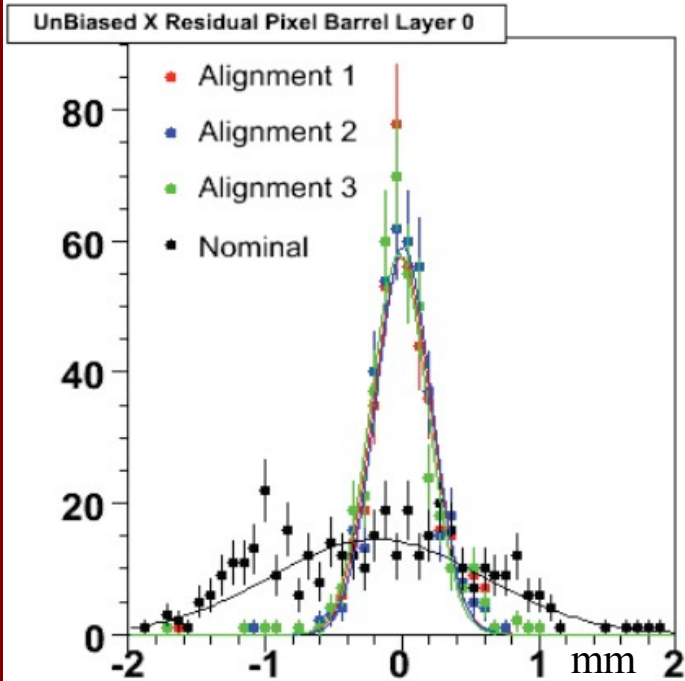


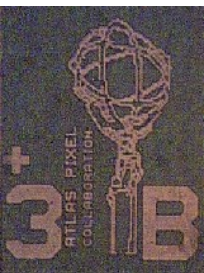


Data: Cosmics!

• Cosmic rays used for

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- Alignment

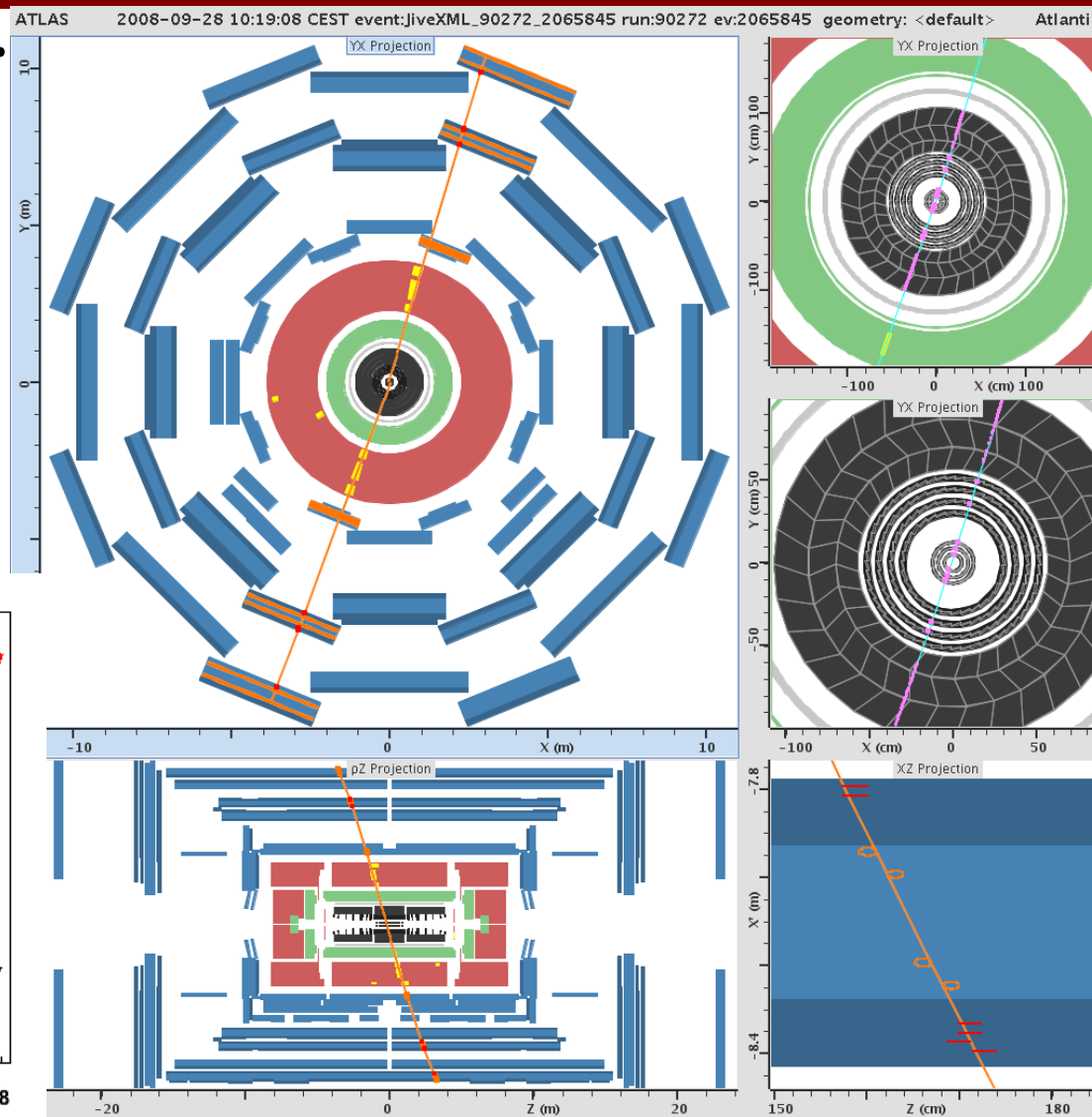
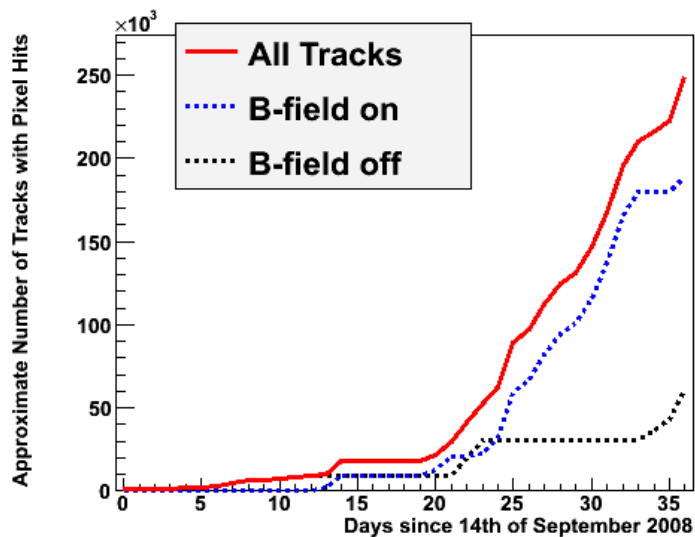


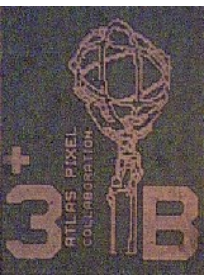


Data: Cosmics!

• Cosmic rays used for

- Noise studies
- Cluster studies
- Alignment
- Physics?

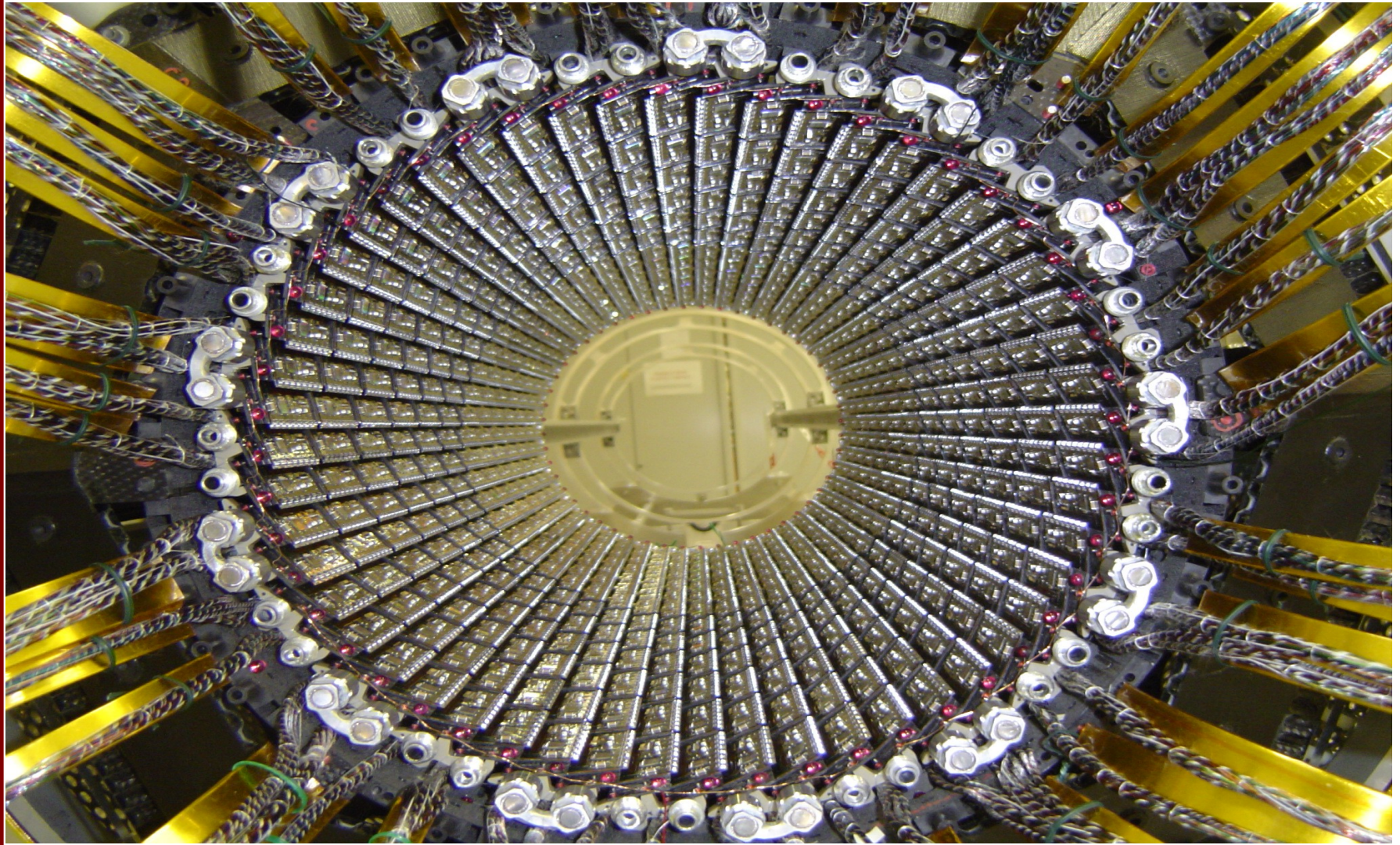


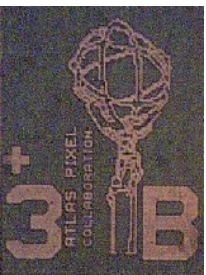


Summary

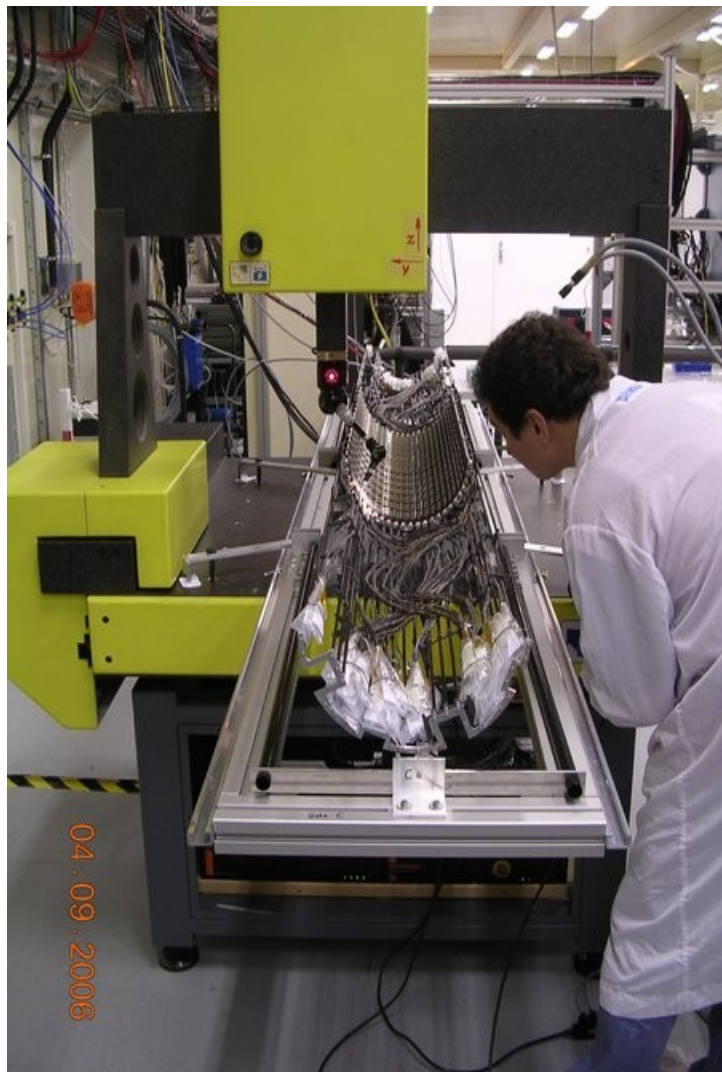
- Full Pixel Detector construction completed
- installed in the Inner Detector since 27th June 07
- Complex system, many challenges overcome
- Operation & Commissioning with Cosmic Rays ongoing
- Waiting for stable LHC beams and full collisions in ATLAS!!

Pixel Outlook

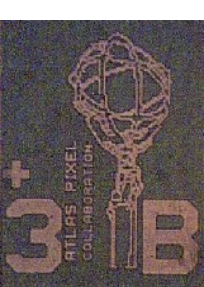




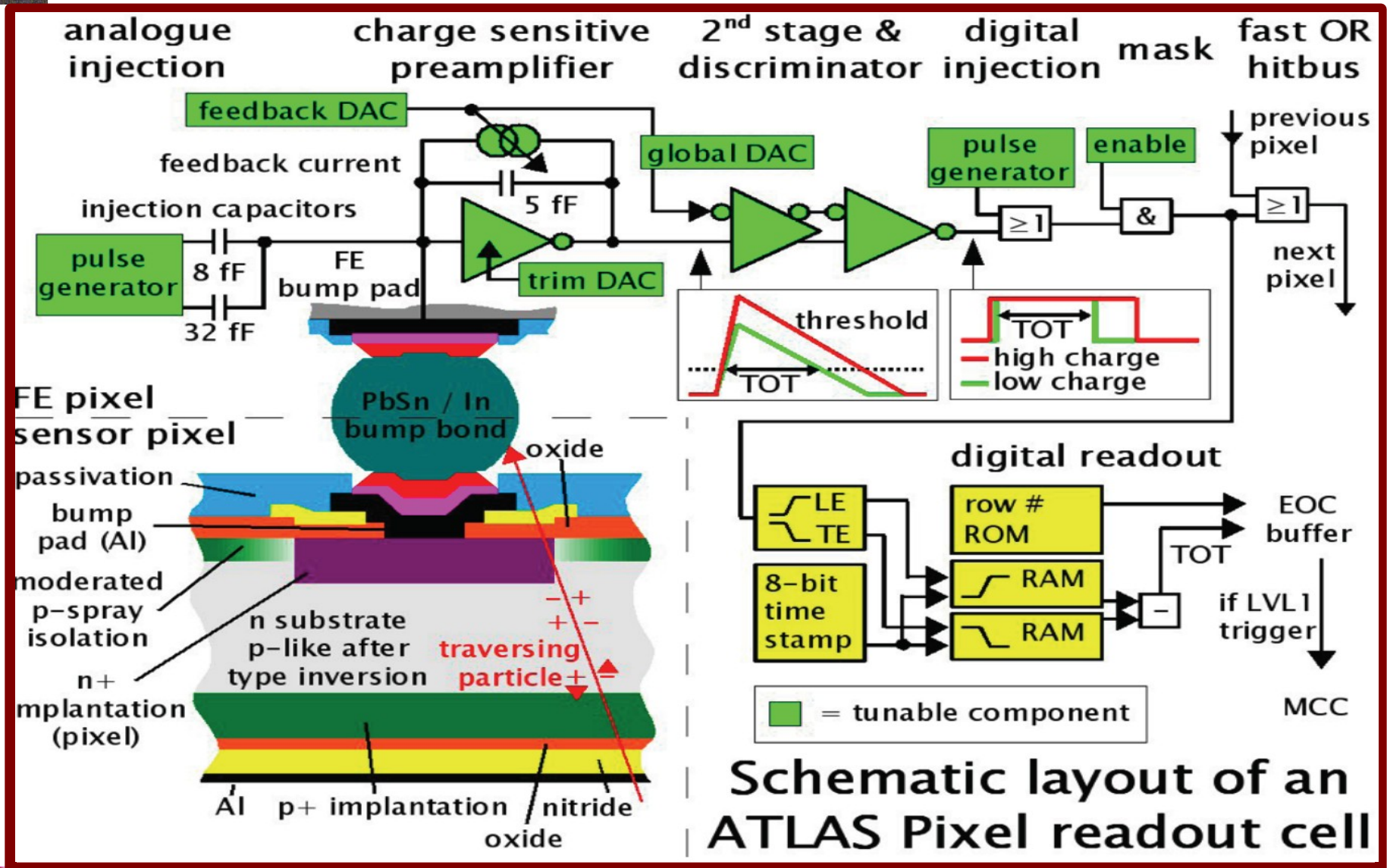
Metrologie



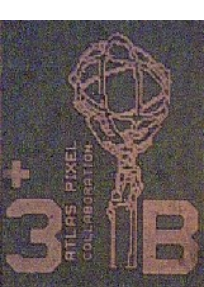
- careful survey of all components
- survey dots on sectors
- measurements on each module to record distortions (bow, twist)
- ruby ball targets on each stave
- survey of half-shells
- detailed material recording
- used as input for “as build” detector description and alignment constraint



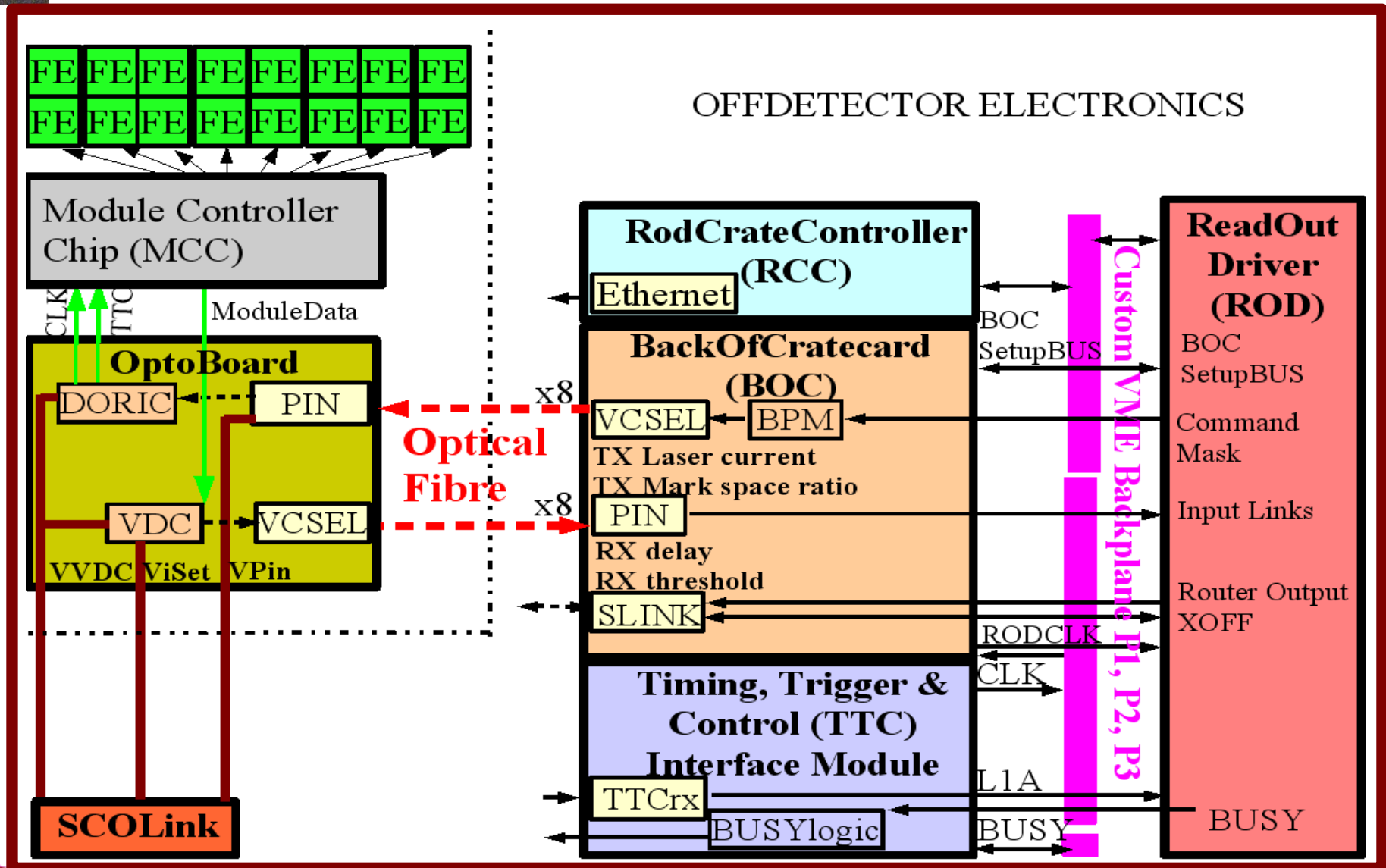
PixReadOut

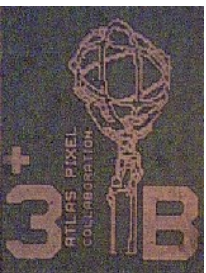


Schematic layout of an ATLAS Pixel readout cell



PixReadOut





DAQ Crates

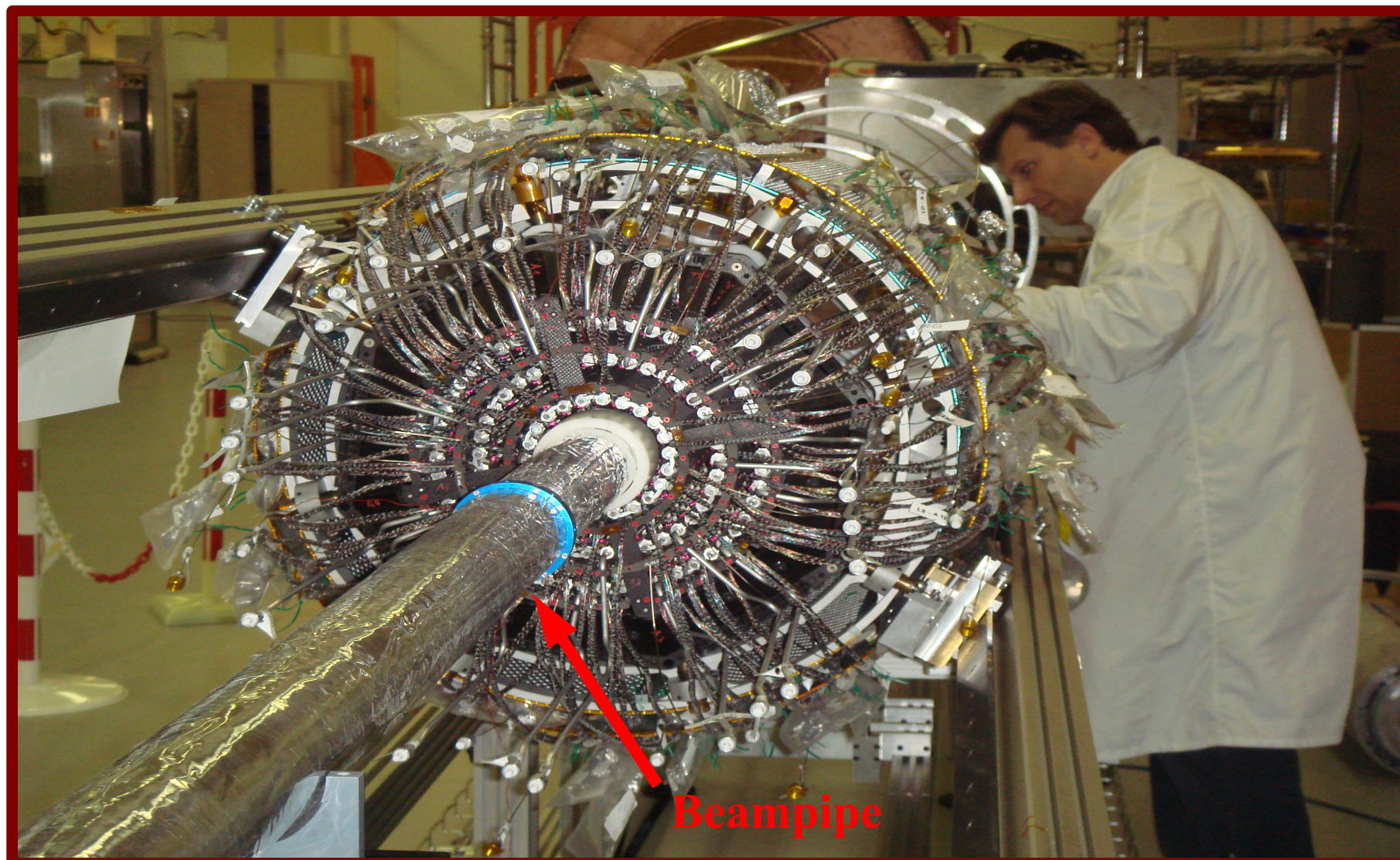


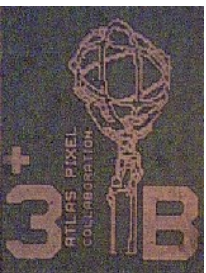
Cosmic Test



- large scale (10%) system test
- emphasis on service and DAQ test
- uses final service and cooling setup, including Service Quarter Panel
- EndCap A (144 Modules)
- EndCap 90 degrees rotated for better coverage
- scintillator system as trigger
- verified efficiency ($> 99\%$) and noise level ($< 10^{-6}$)

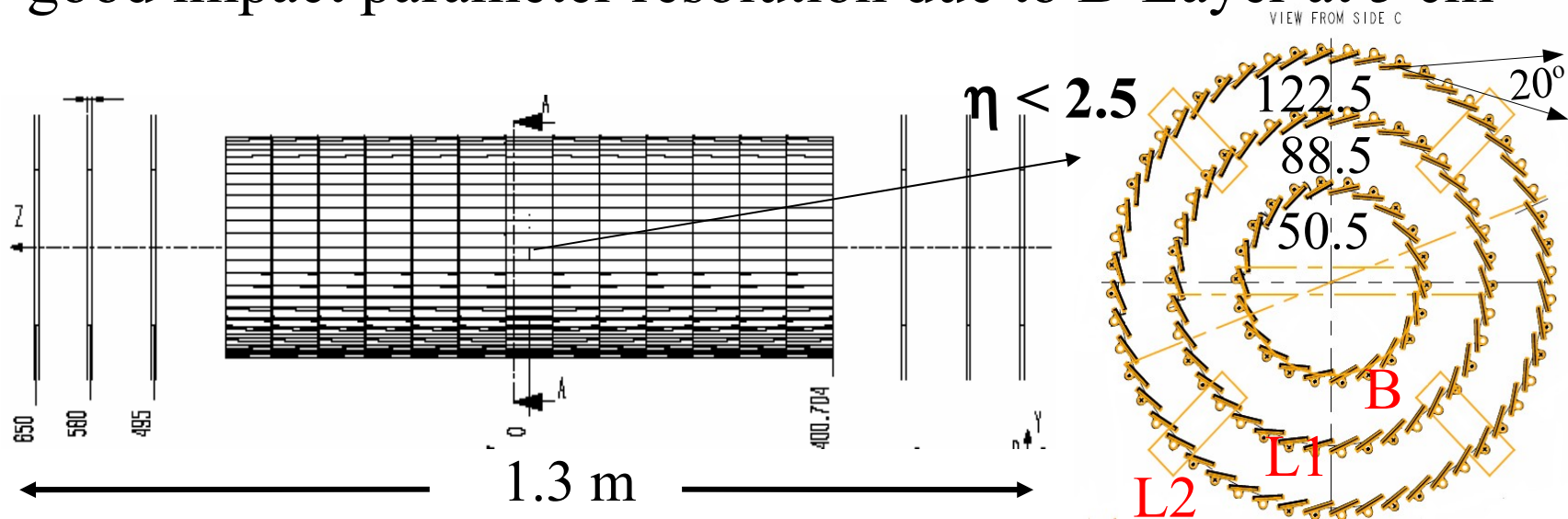
Finished Barrel

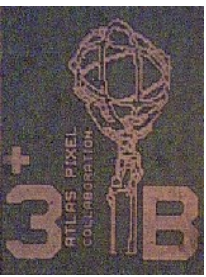




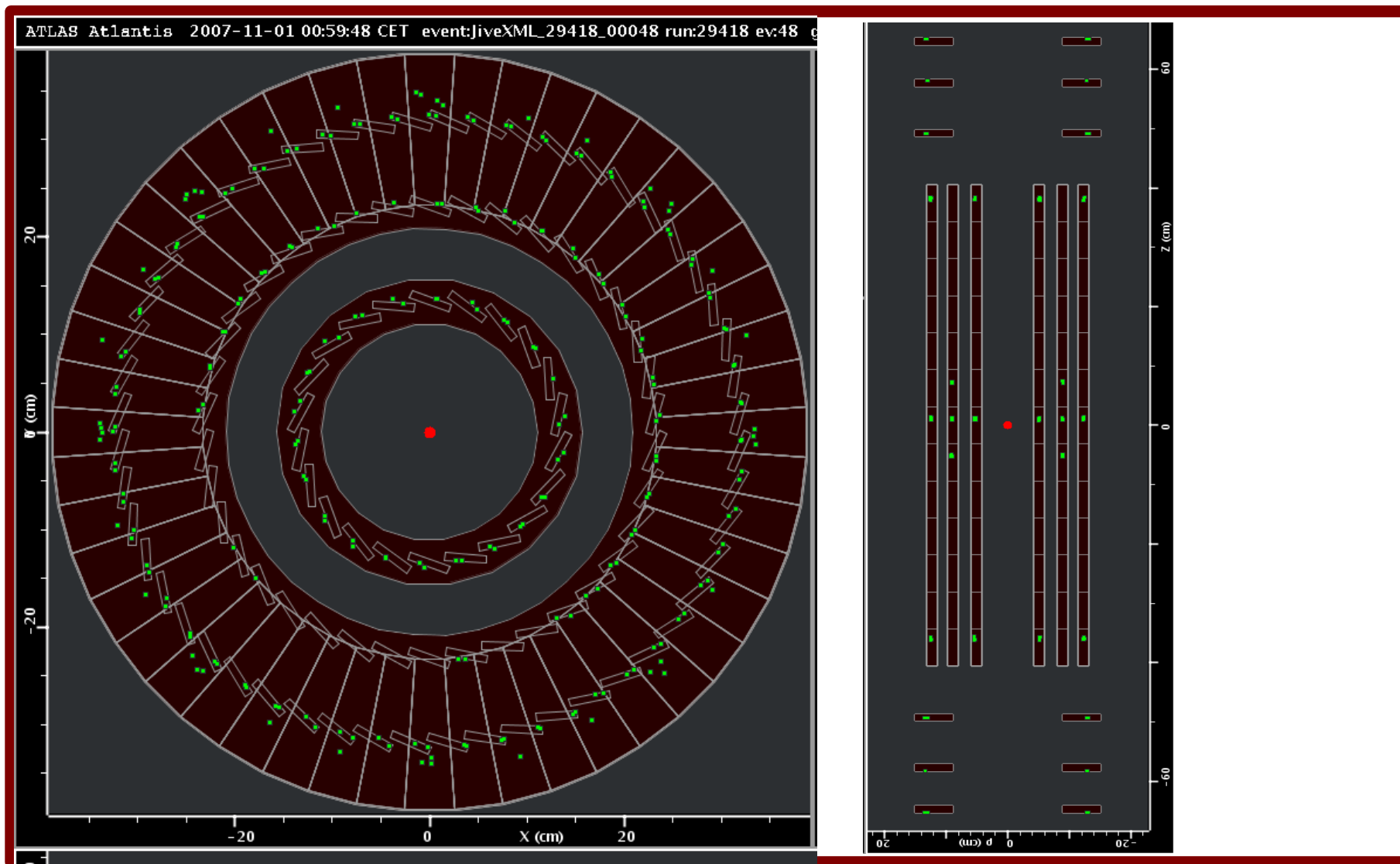
Pixel Overview

- 3 Barrels, 2x3 disks
- Total of 1744 Modules, organized in Staves and Sectors
- 46080 channels per Module, total ~ 80 million channels
- minimize confusion in pattern recognition (Occupancy 10^{-4} , Noise $< 10^{-6}$)
- good impact parameter resolution due to B-Layer at 5 cm

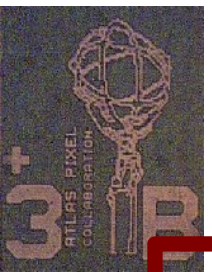




Pixel Hit Map

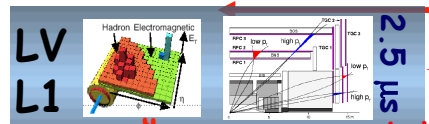


ATLAS Trigger/DAQ Overview



Trigger

40 MHz

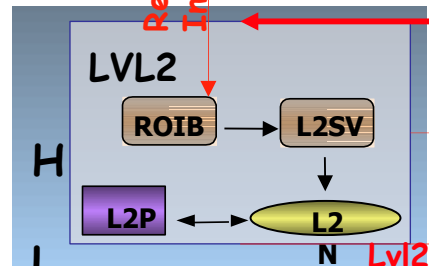


Lvl1 acc = 75 kHz

RoI data = 1-2%

75 kHz

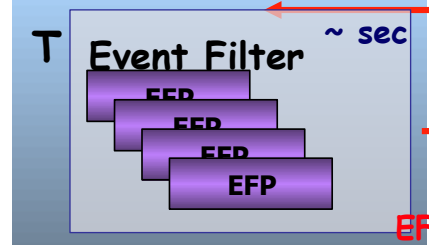
RoI Builder
L2 Supervisor
L2 N/work
L2 Proc Unit



Lvl2 acc = ~2 kHz

~2 kHz

Event Filter Processors



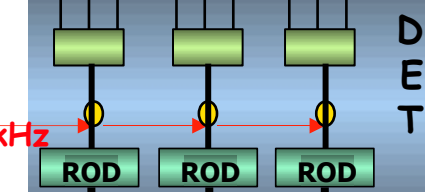
EFact = ~0.2 kHz

~ 200 Hz

Calo
MuTrCh Other detectors

DAQ

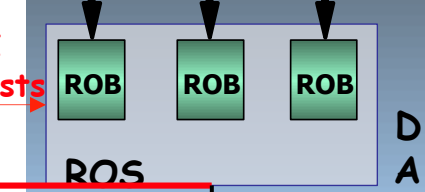
FE Pipelines



Read-Out Drivers
Read-Out Links
Read-Out Buffers

120 GB/s

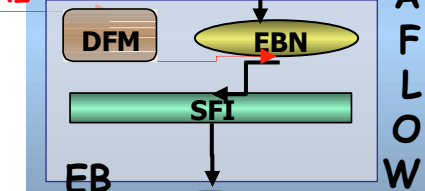
Event data
RoI requests



Read-Out Sub-systems

~2+4 GB/s

Dataflow Manager
Event Building N/work



Sub-Farm Input
Event Builder
Event Filter N/work

Sub-Farm Output

~ 300 MB/s

